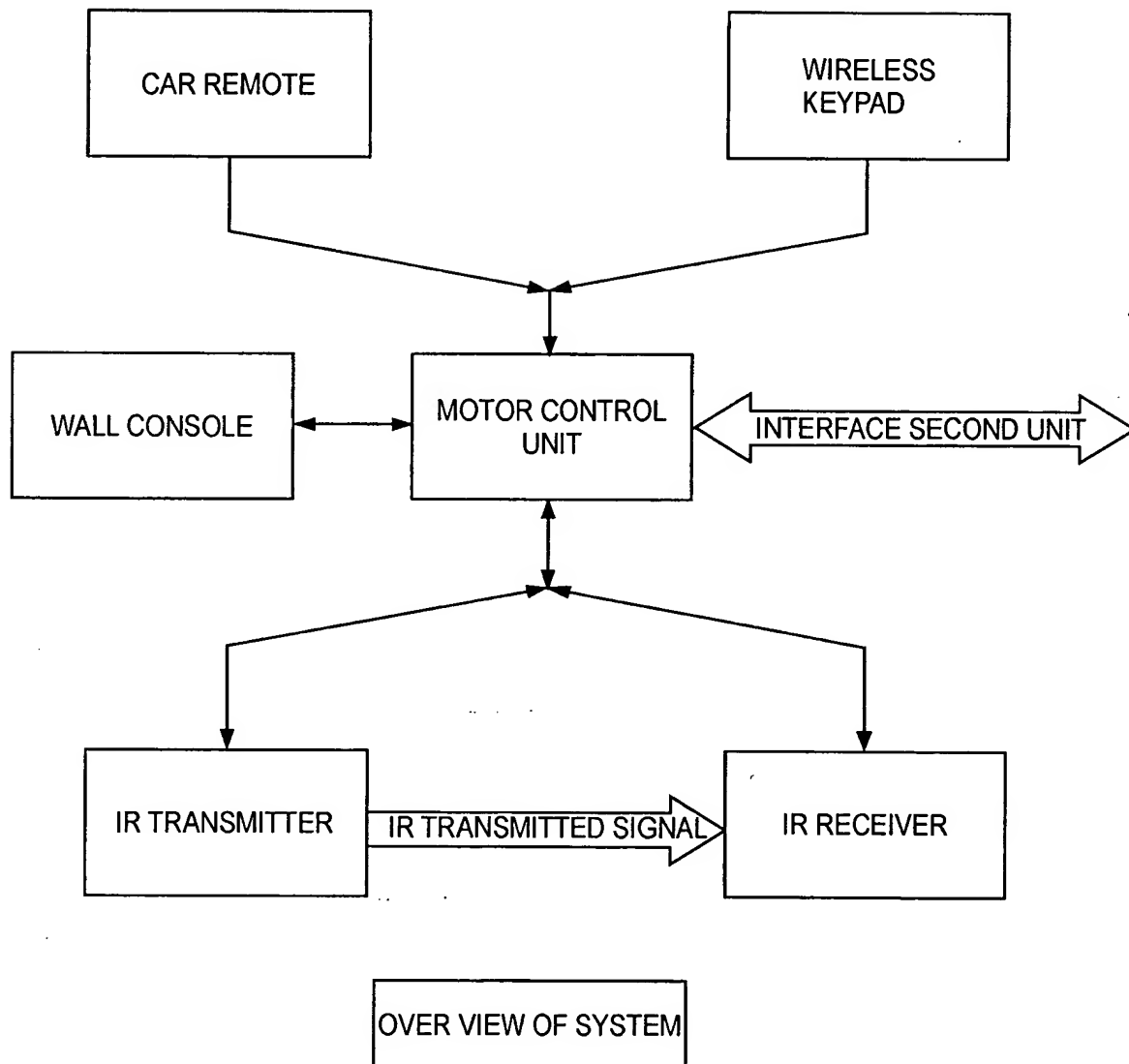




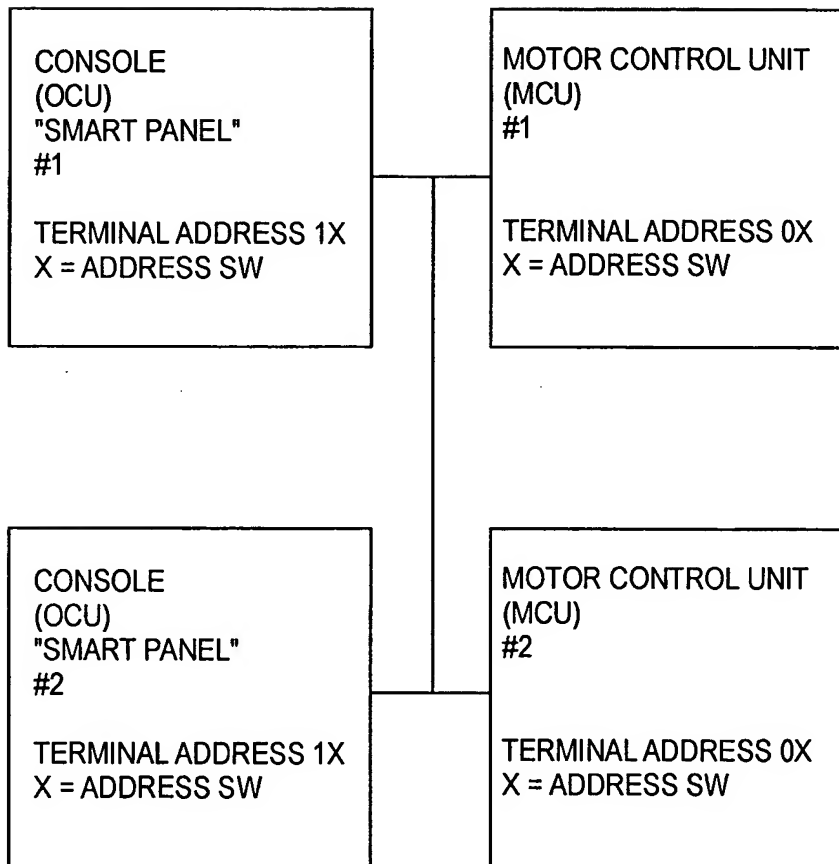
1/45

Fig. 1



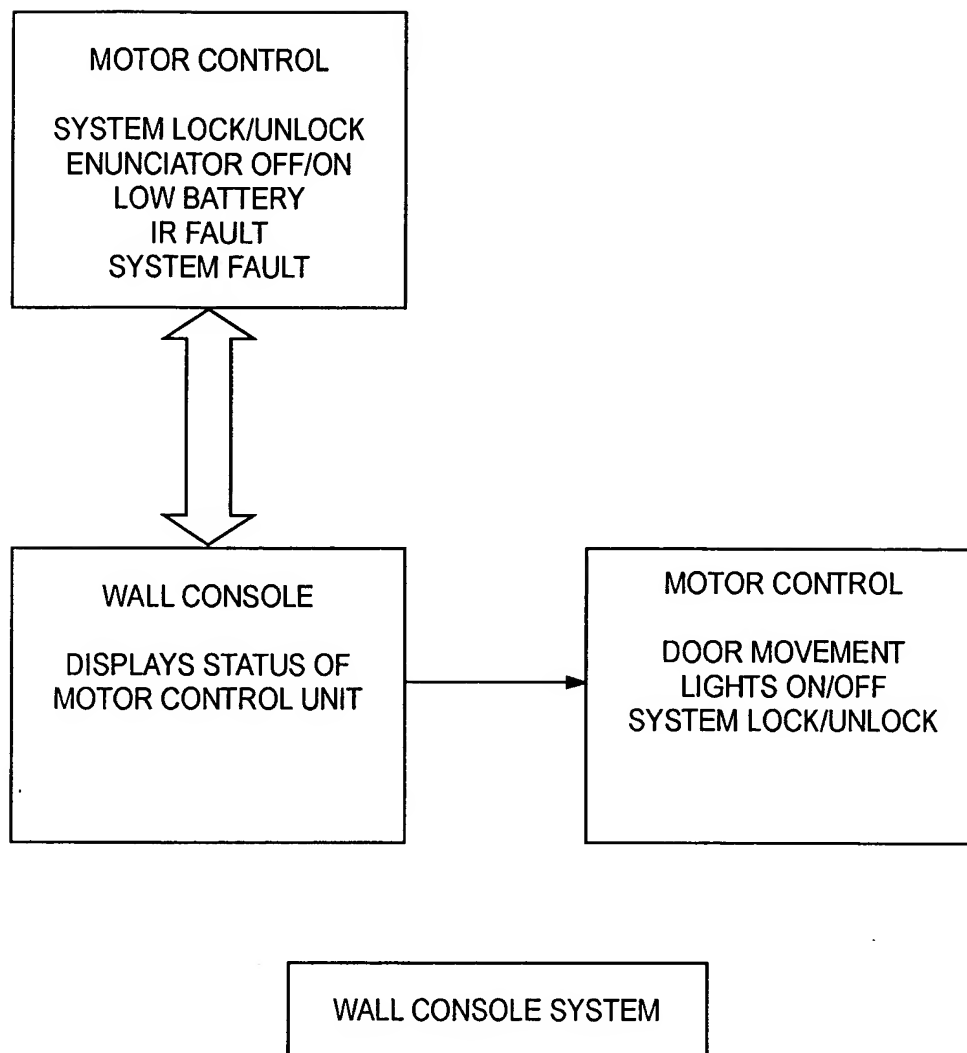
2/45

Fig. 2



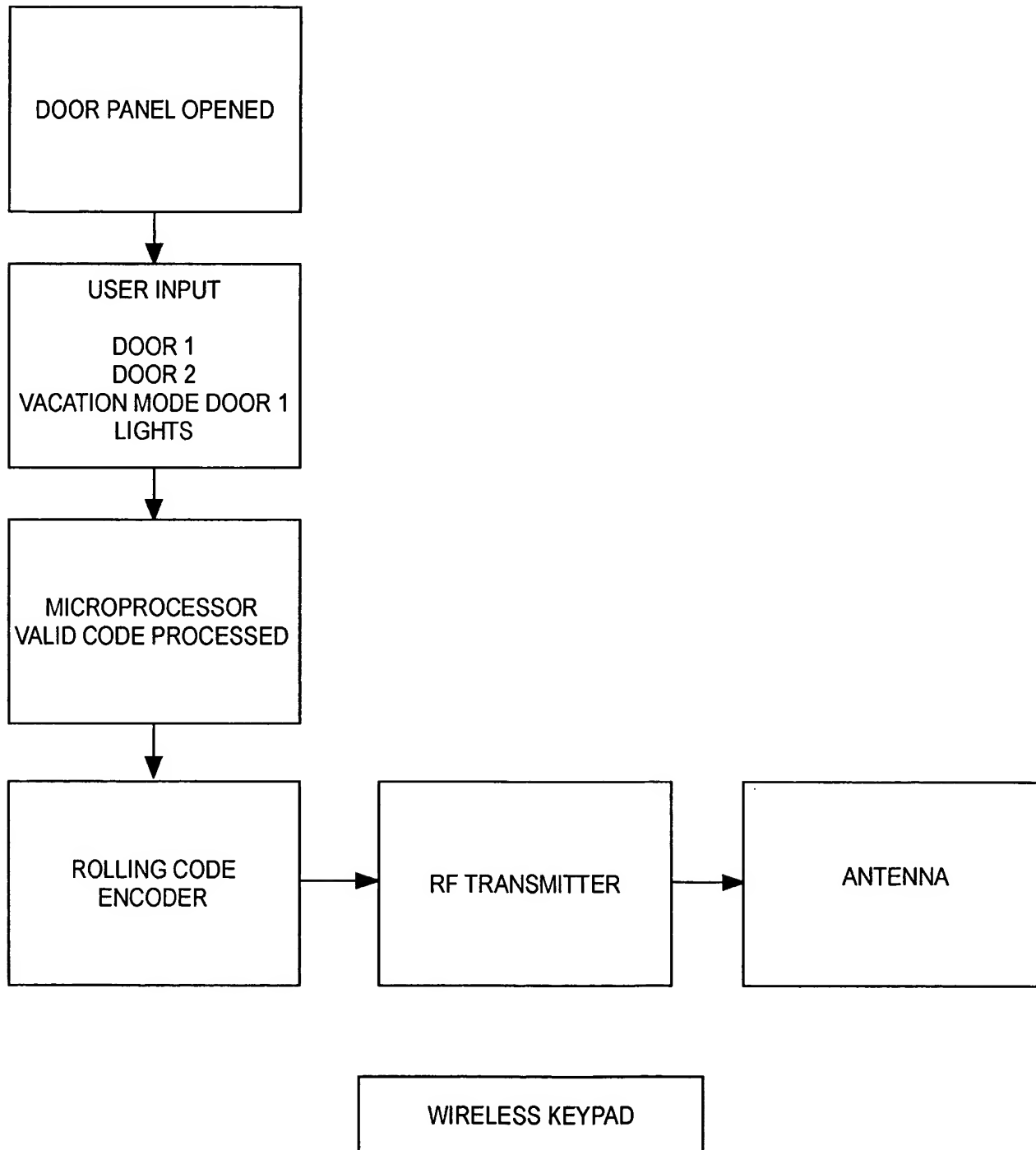
3/45

Fig. 3



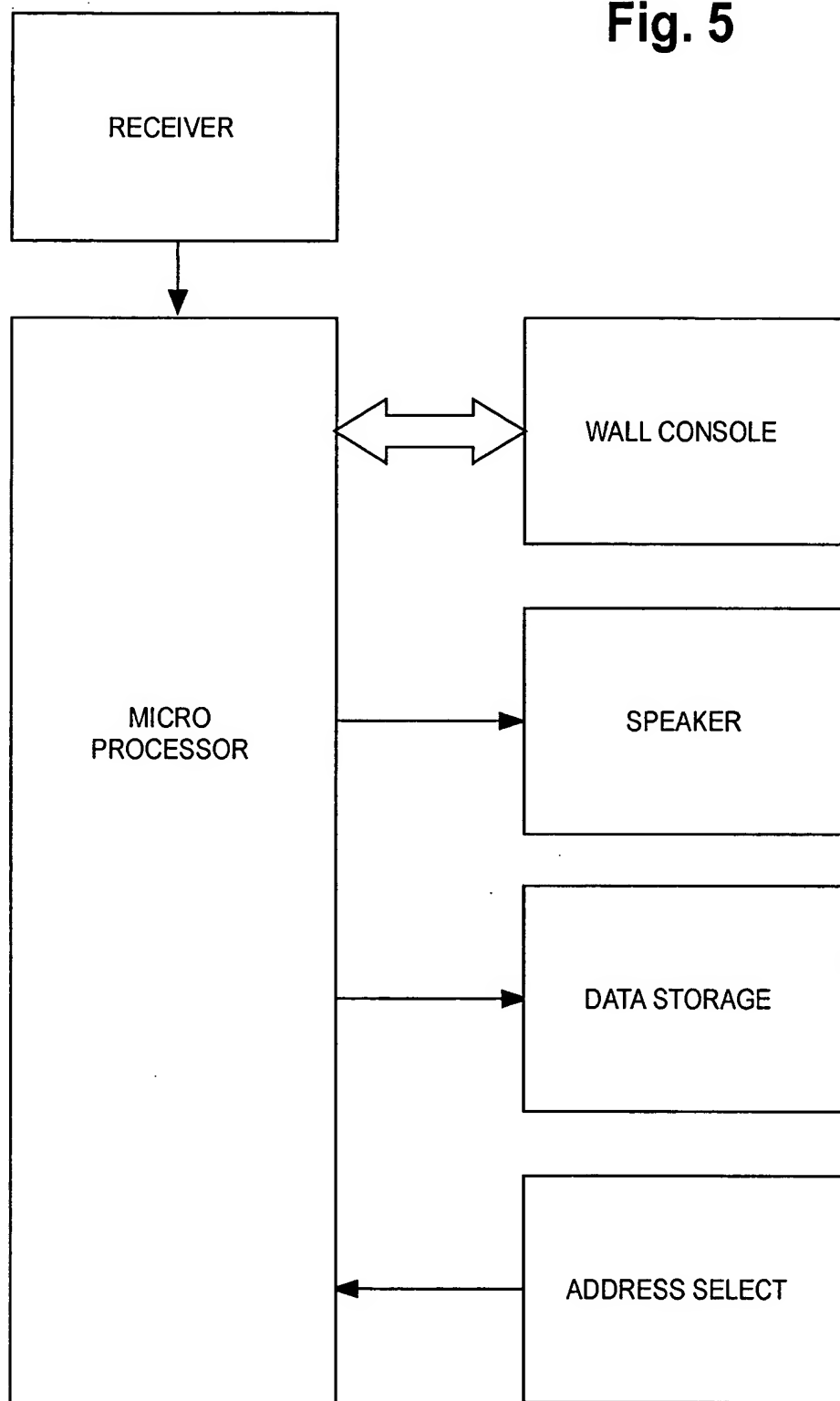
4/45

Fig. 4



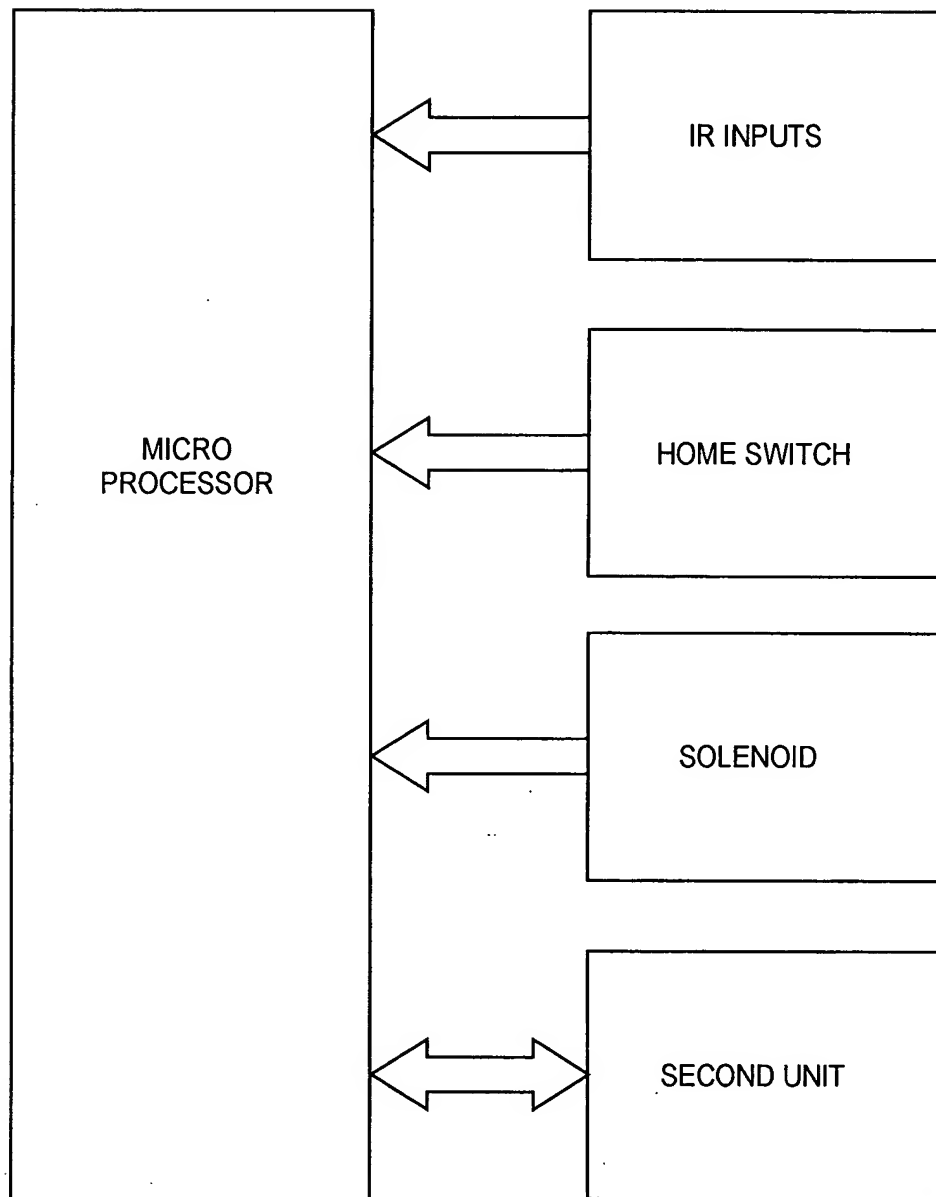
5/45

Fig. 5



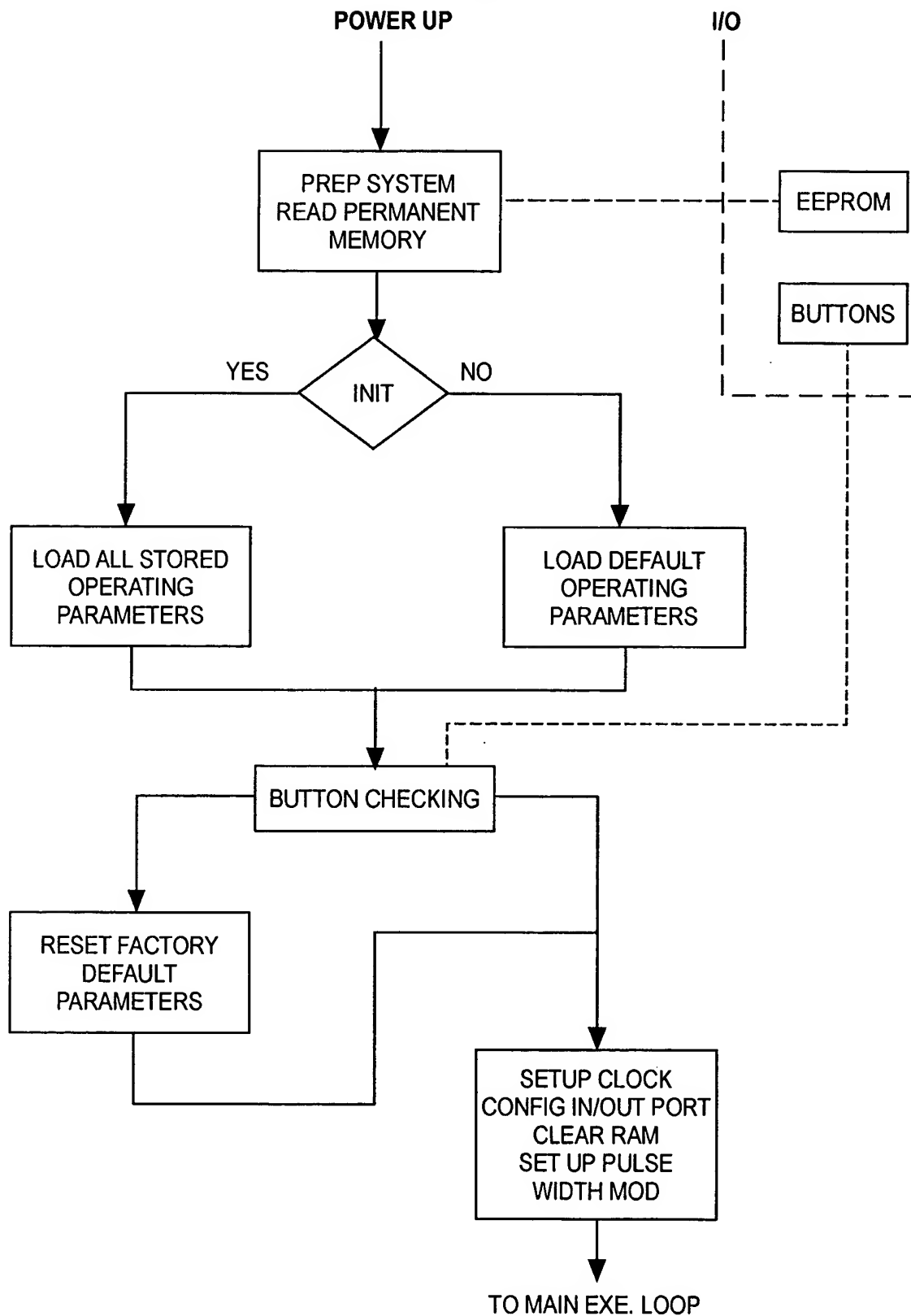
6/45

Fig. 6



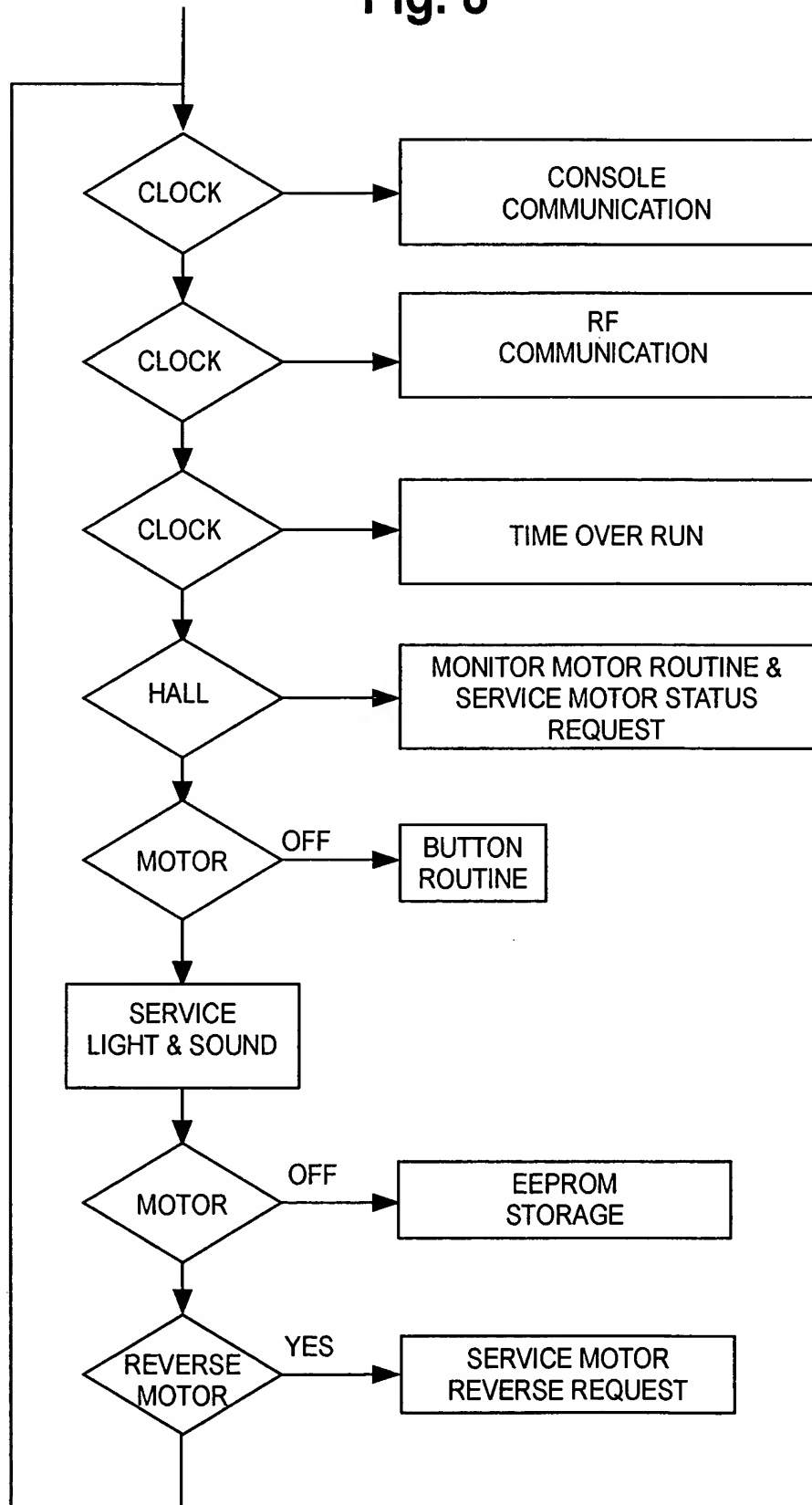
7/45

Fig. 7



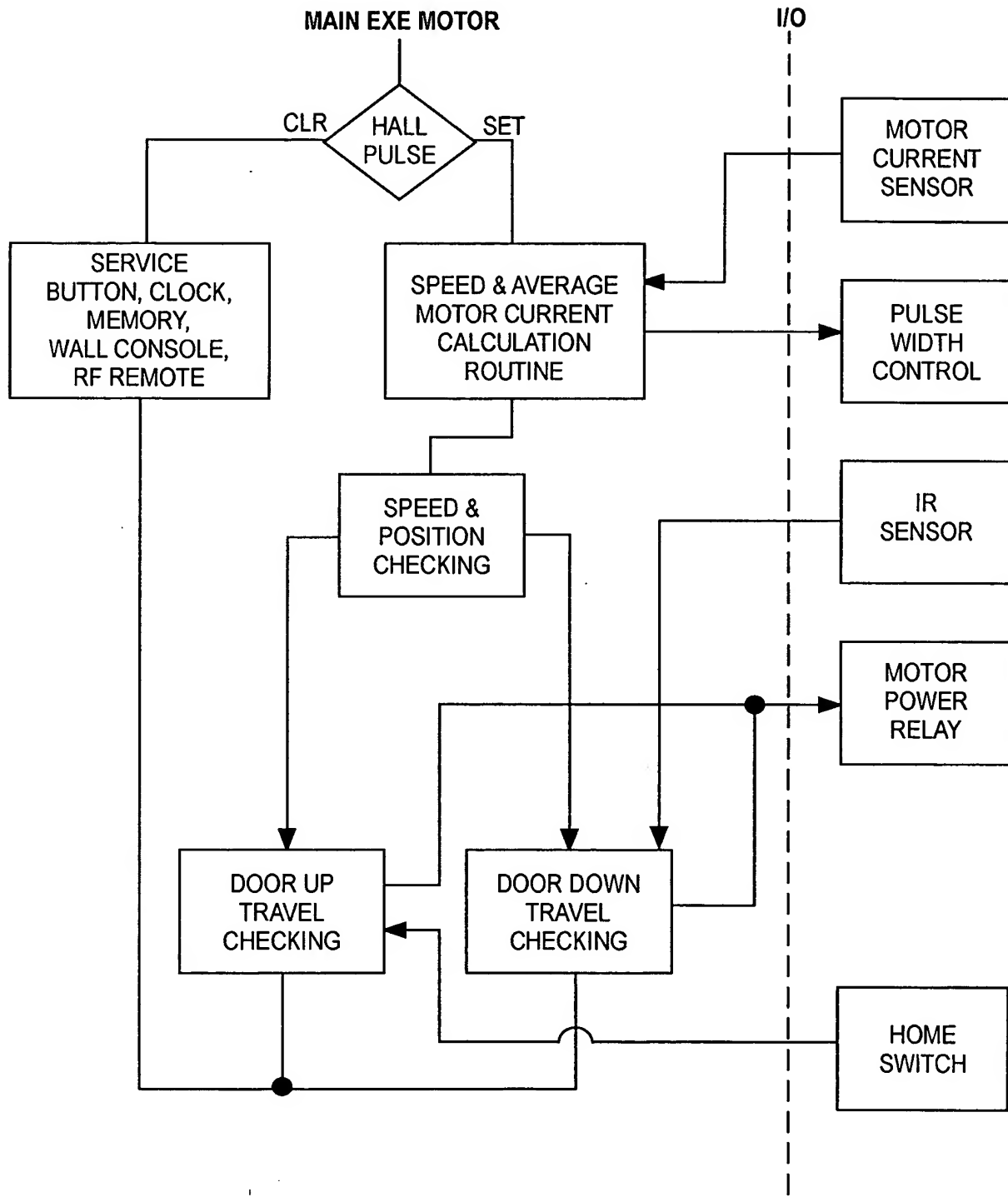
8/45

Fig. 8



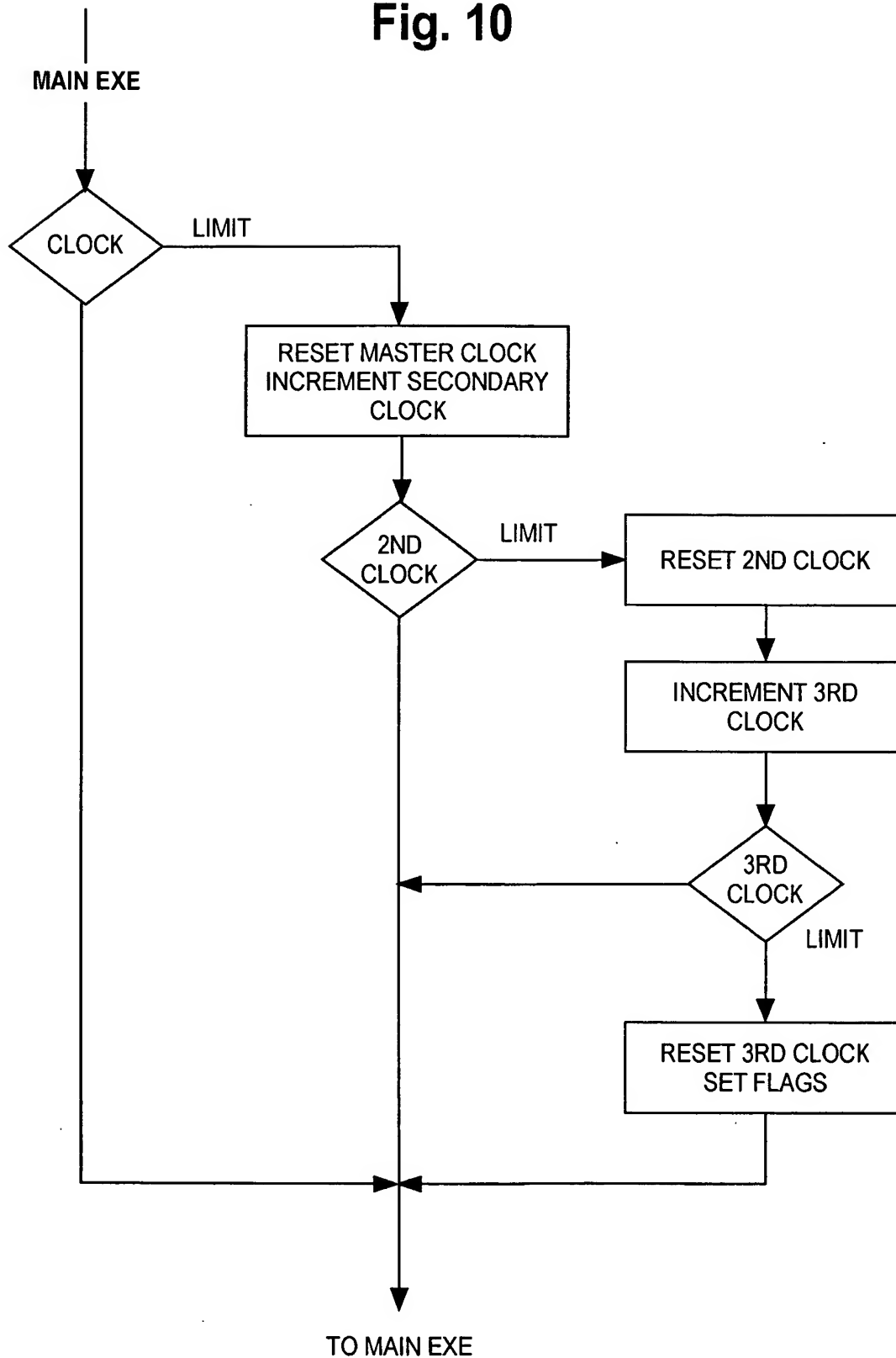
9/45

Fig. 9



10/45

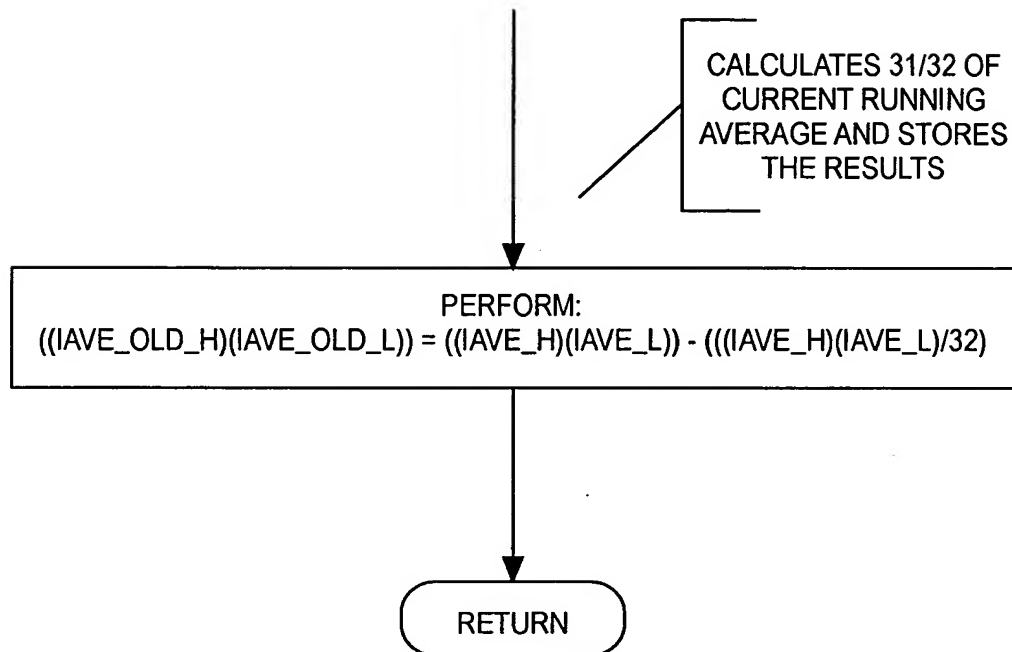
Fig. 10



11/45

Fig. 11

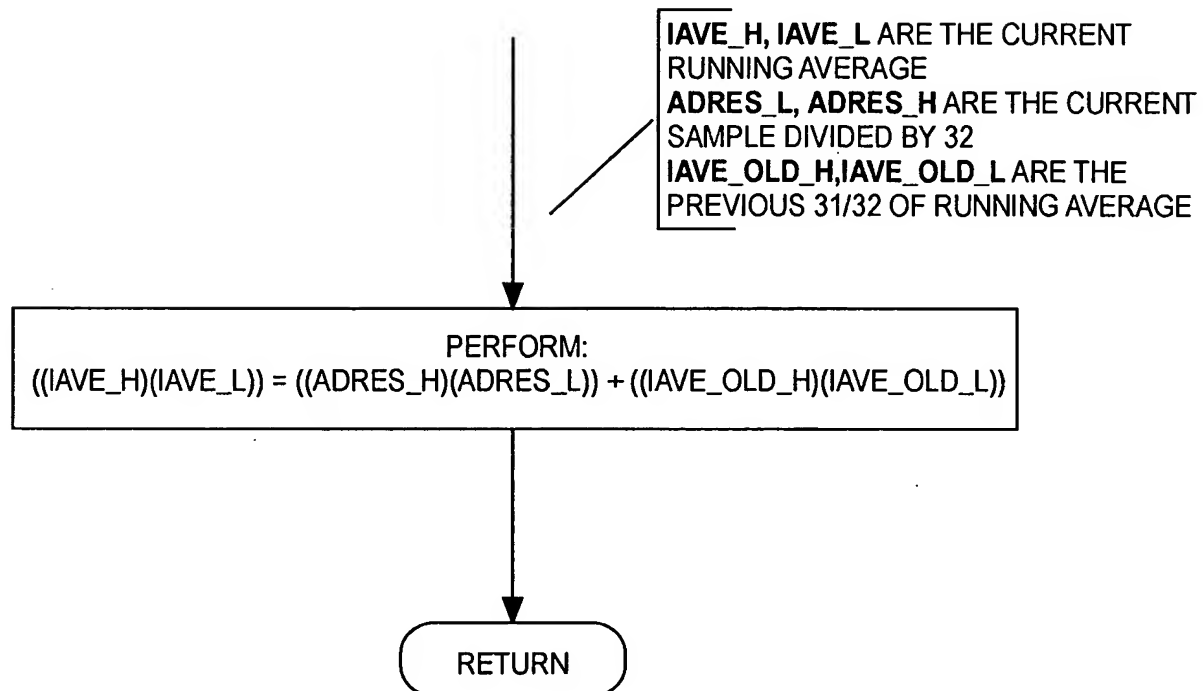
CALC_OLD_AVE



12/45

Fig. 12

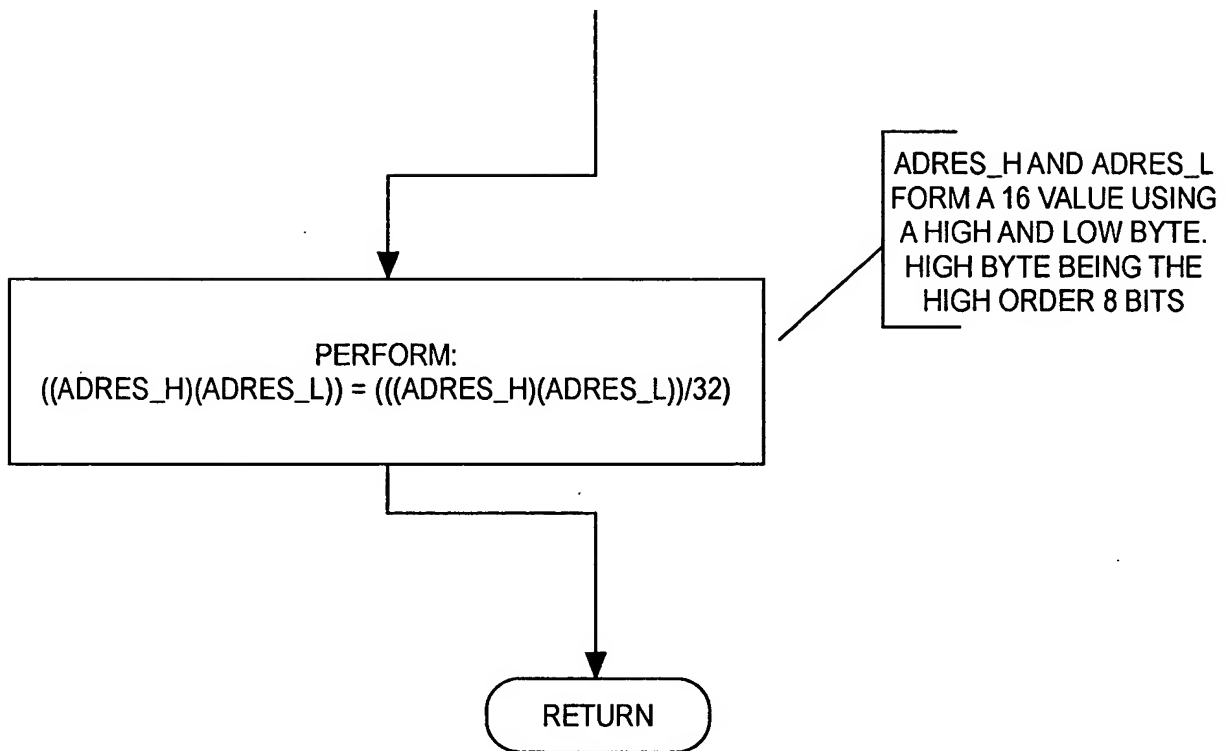
CALC_NEW_AVE



13/45

Fig. 13

AD_DIV_32



14/45

PWMRAMPDOWN

Fig. 14

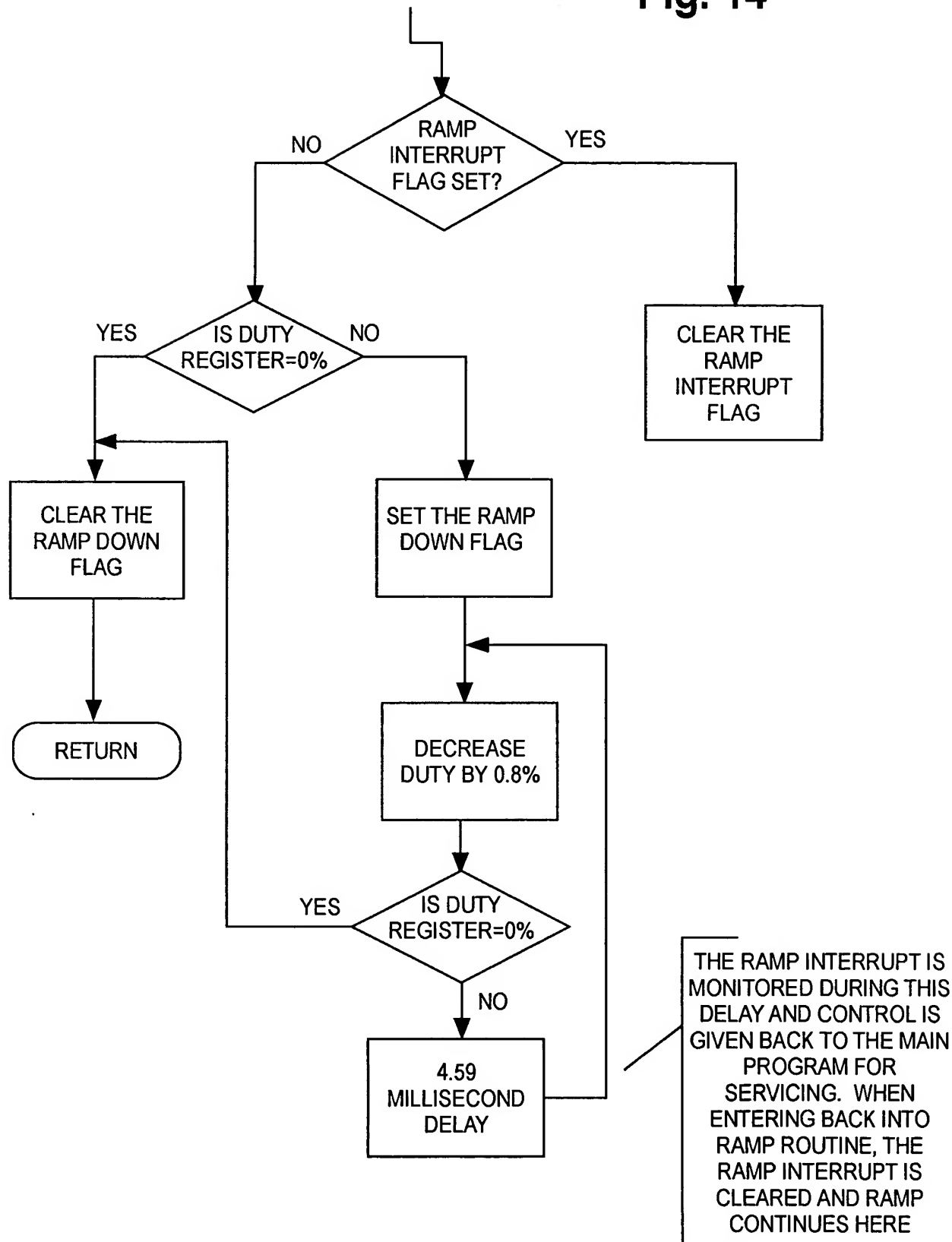
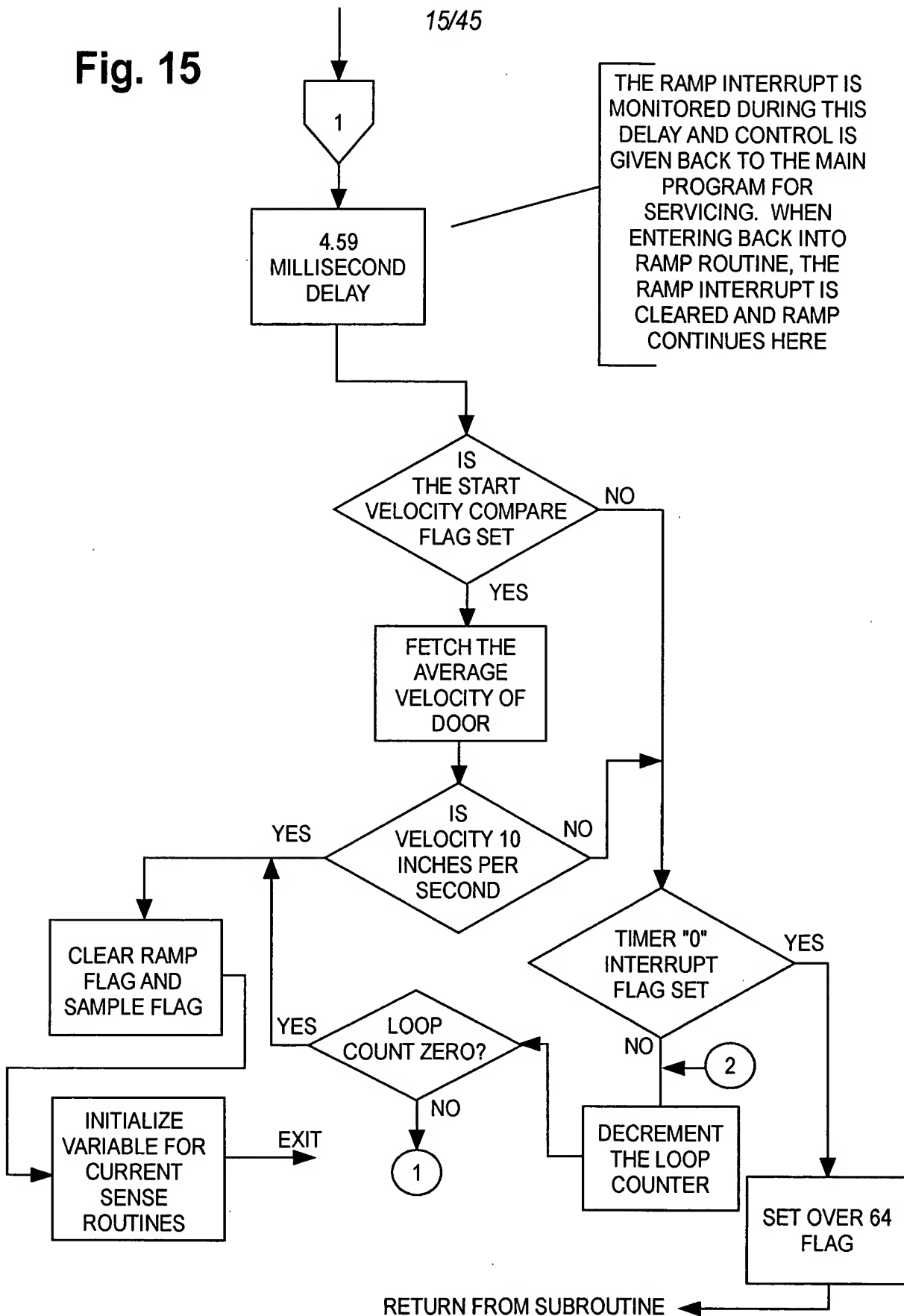


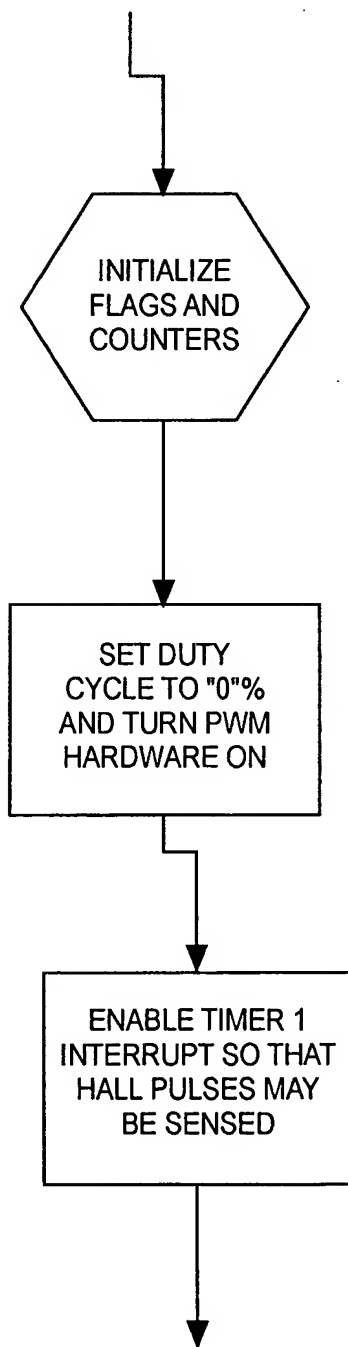
Fig. 15



16/45

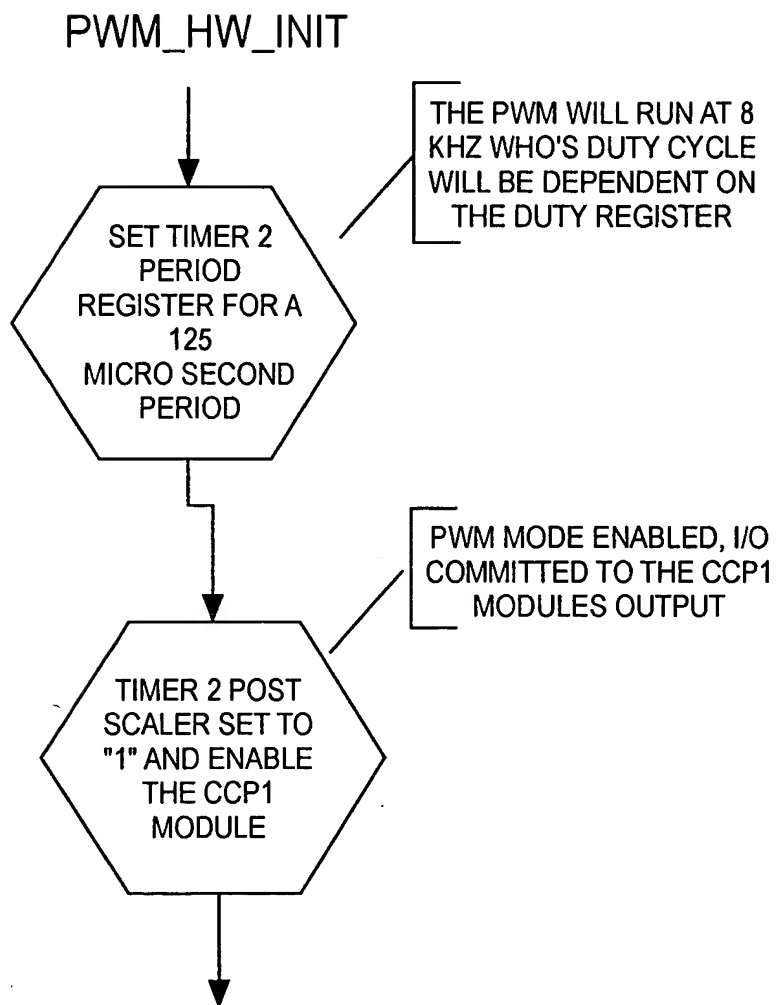
Fig. 16

PWM_RAMP_INIT



17/45

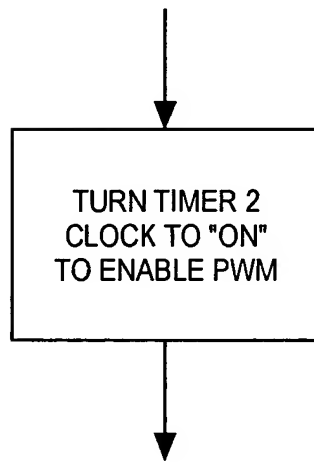
Fig. 17



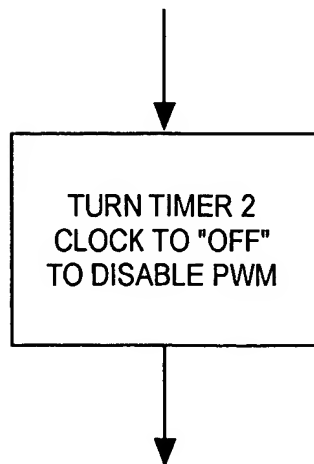
18/45

Fig. 18

PWM_HW_ON



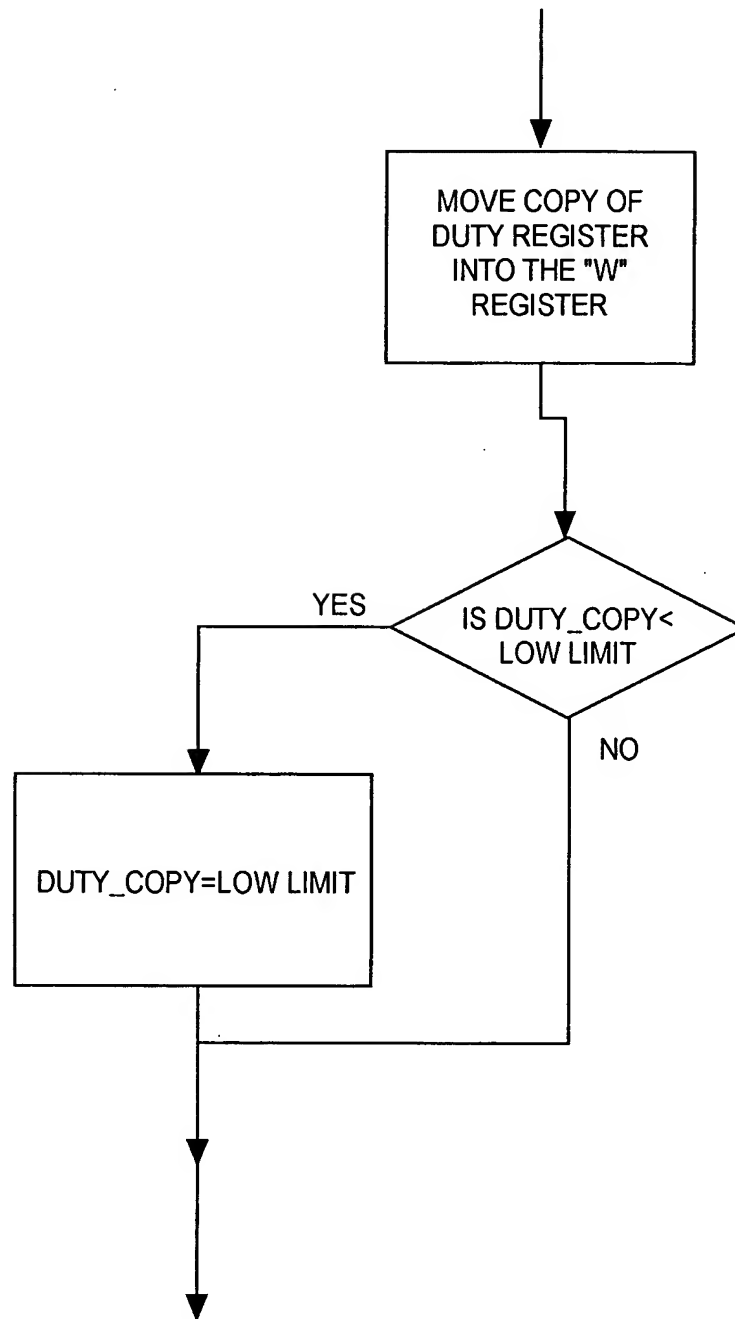
PWM_HW_OFF



19/45

Fig. 19

LOW_CLIP



20/45

Fig. 20

HIGH_CLIP

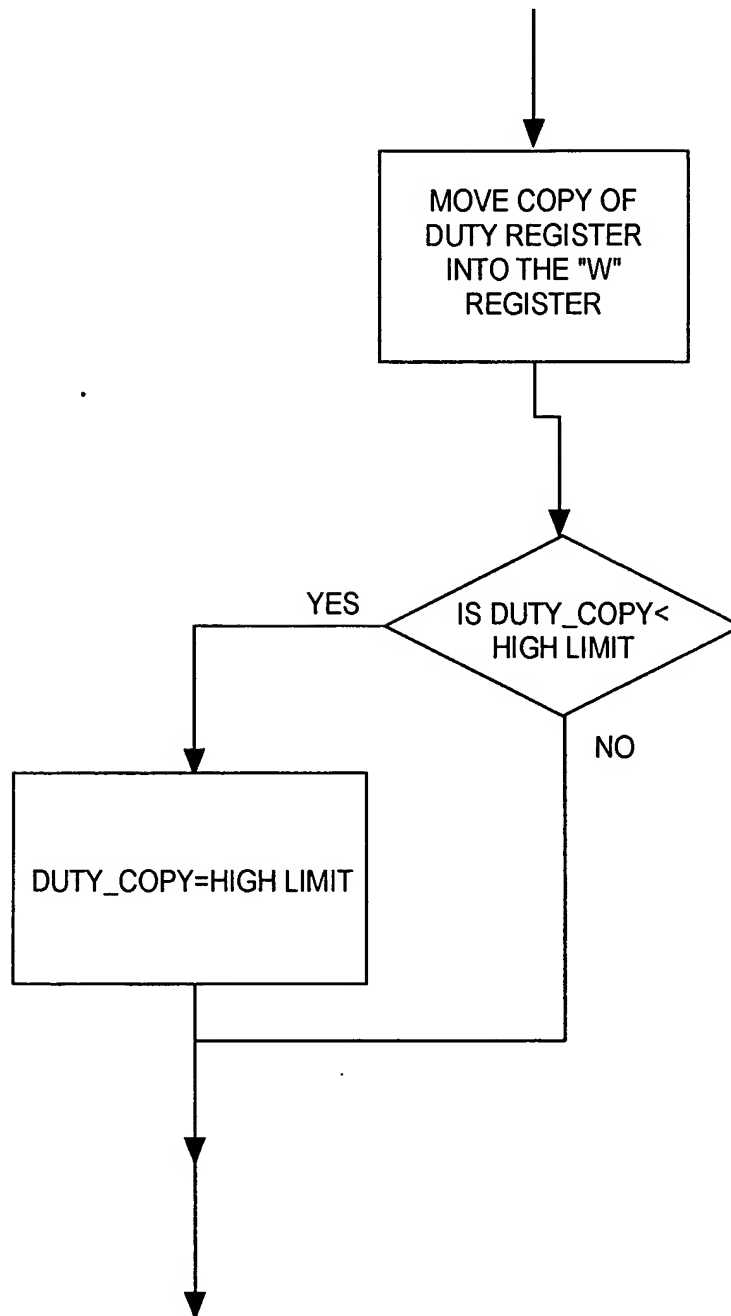
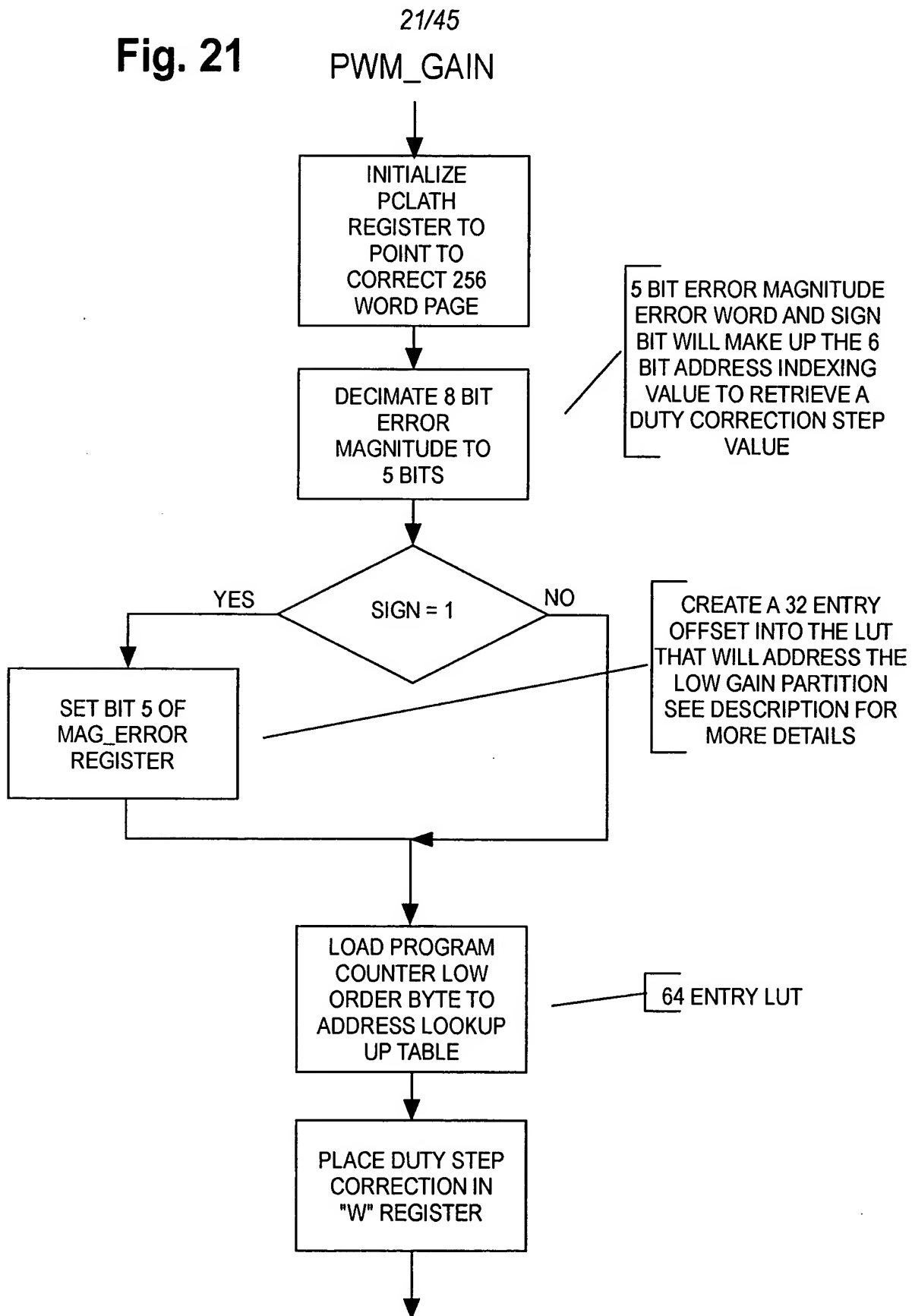
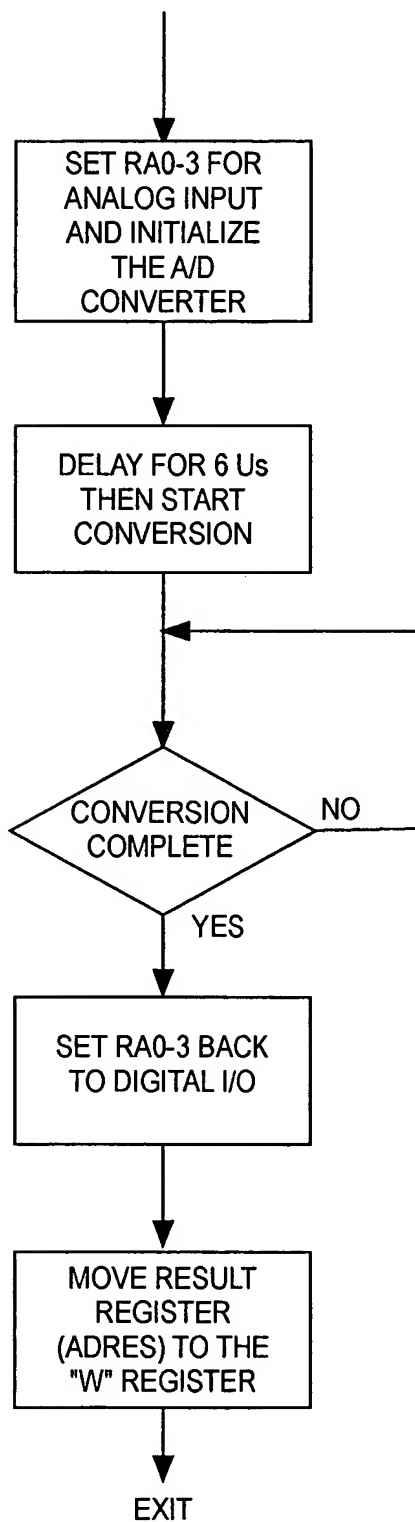


Fig. 21



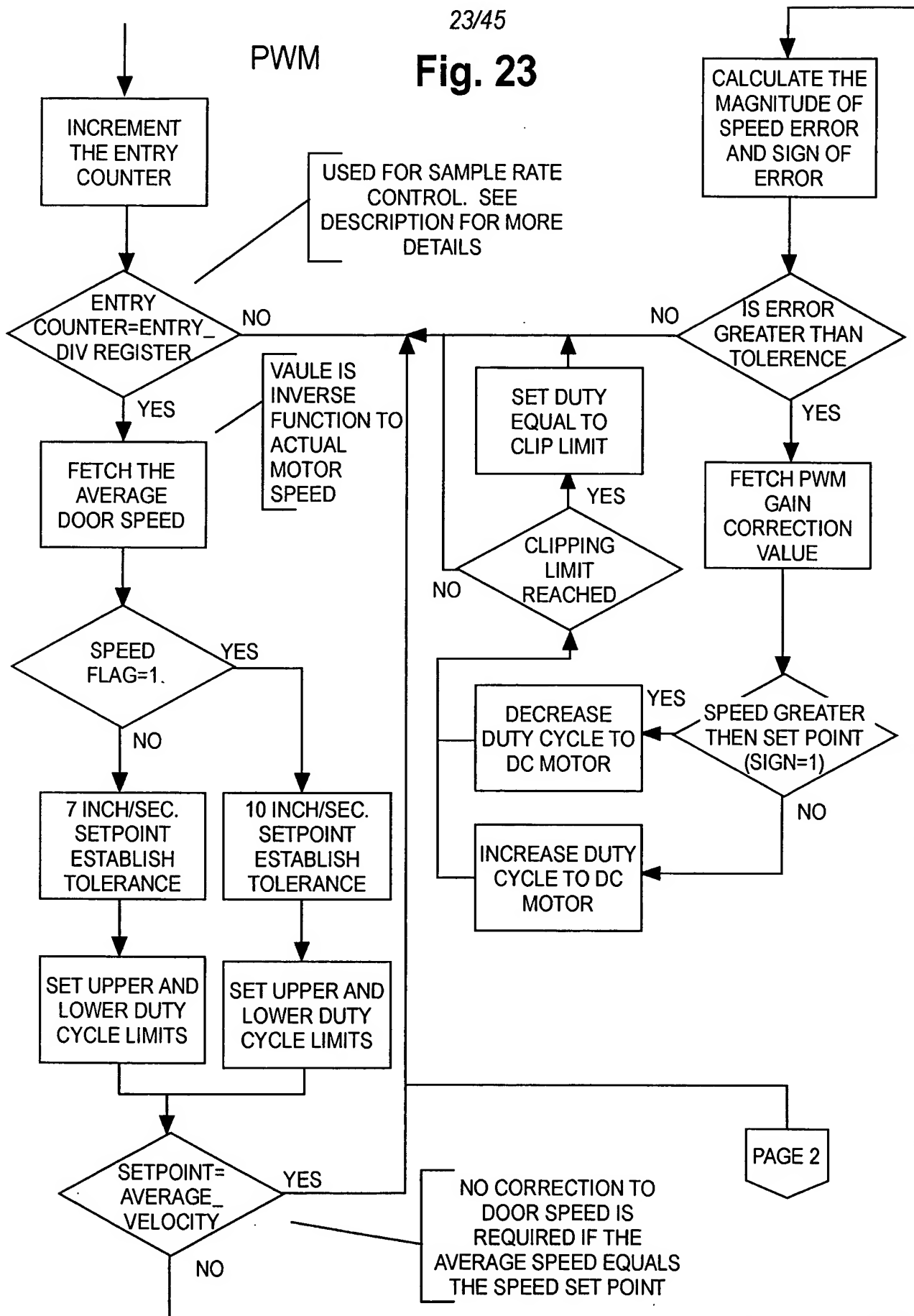
22/45

Fig. 22



23/45

Fig. 23



24/45

Fig. 24

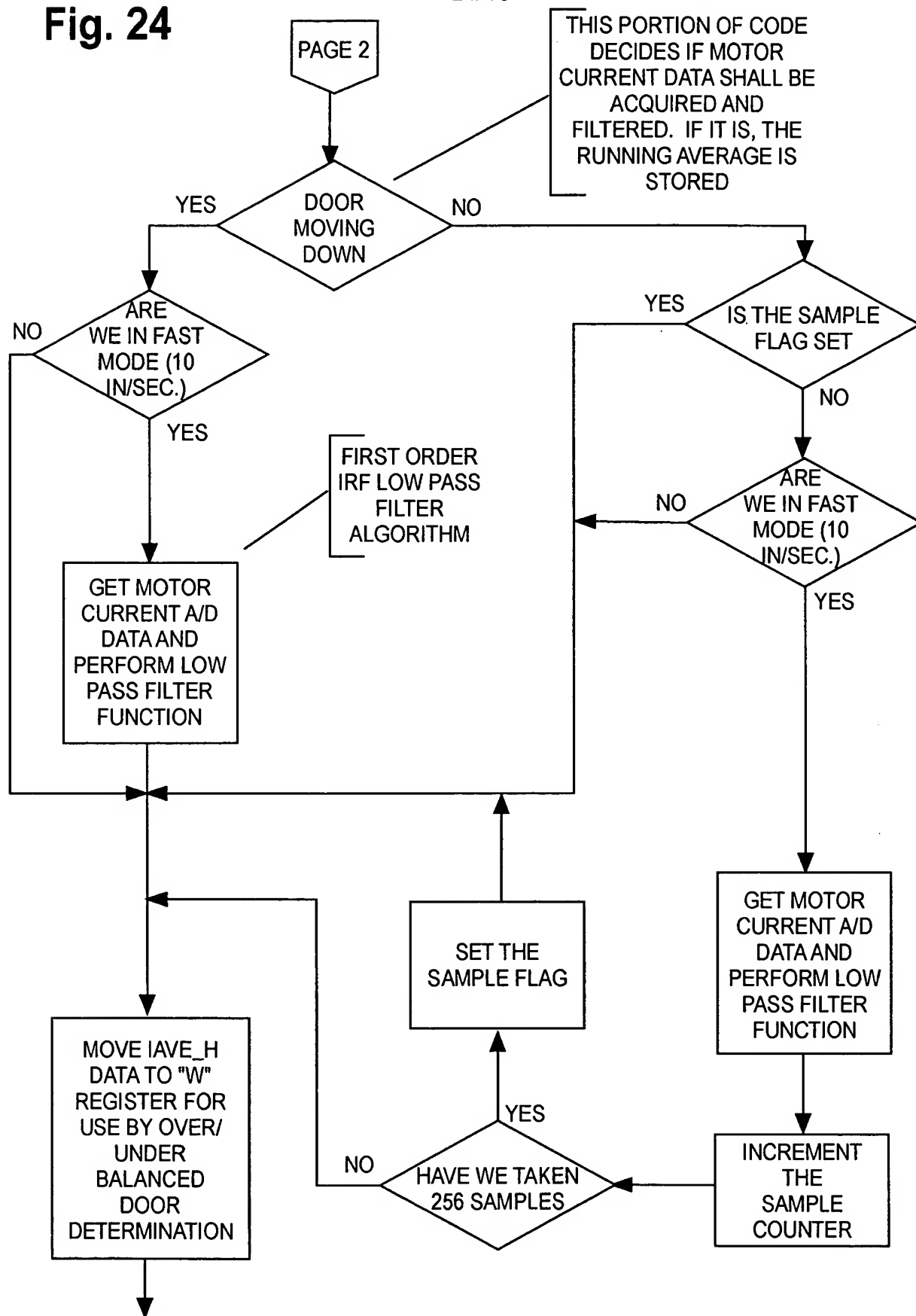
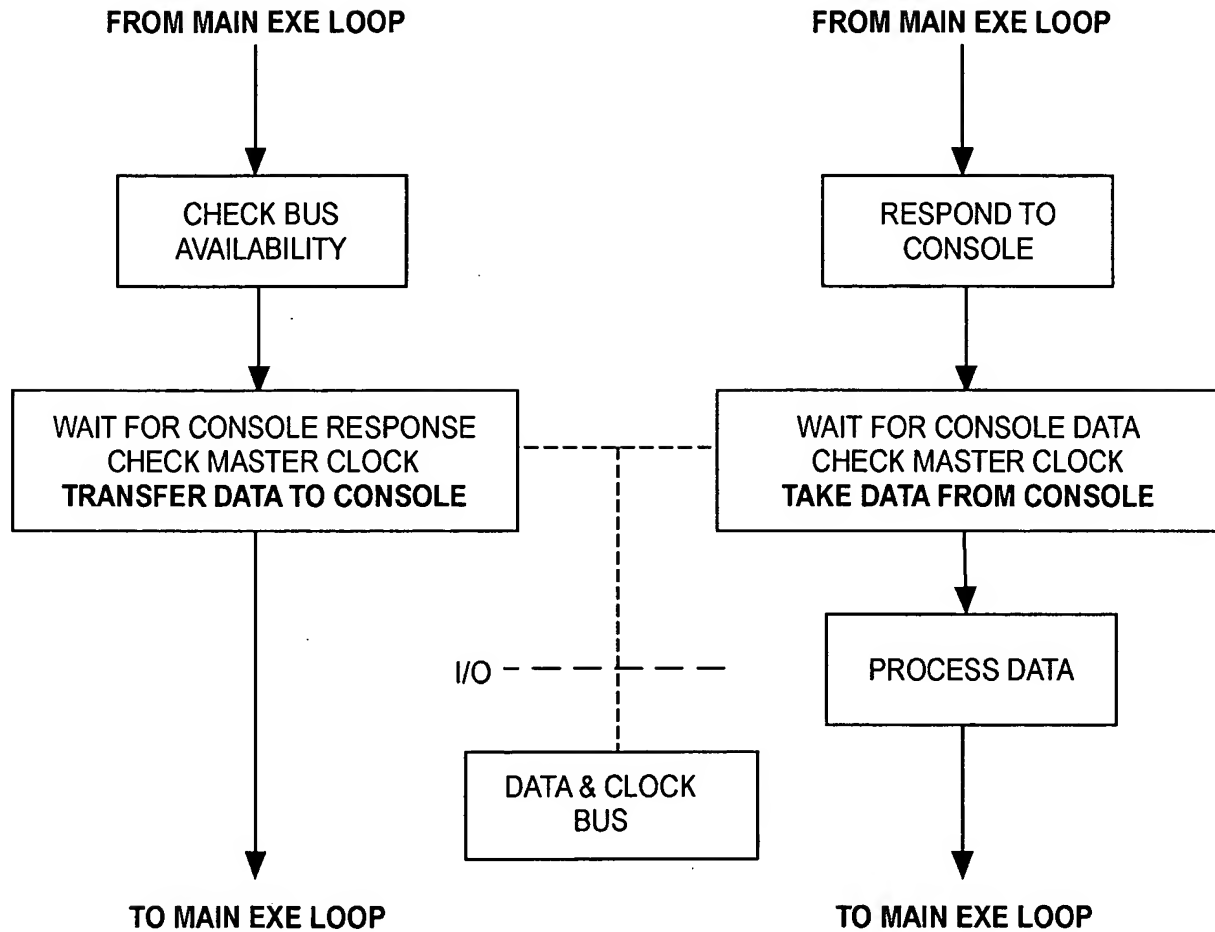
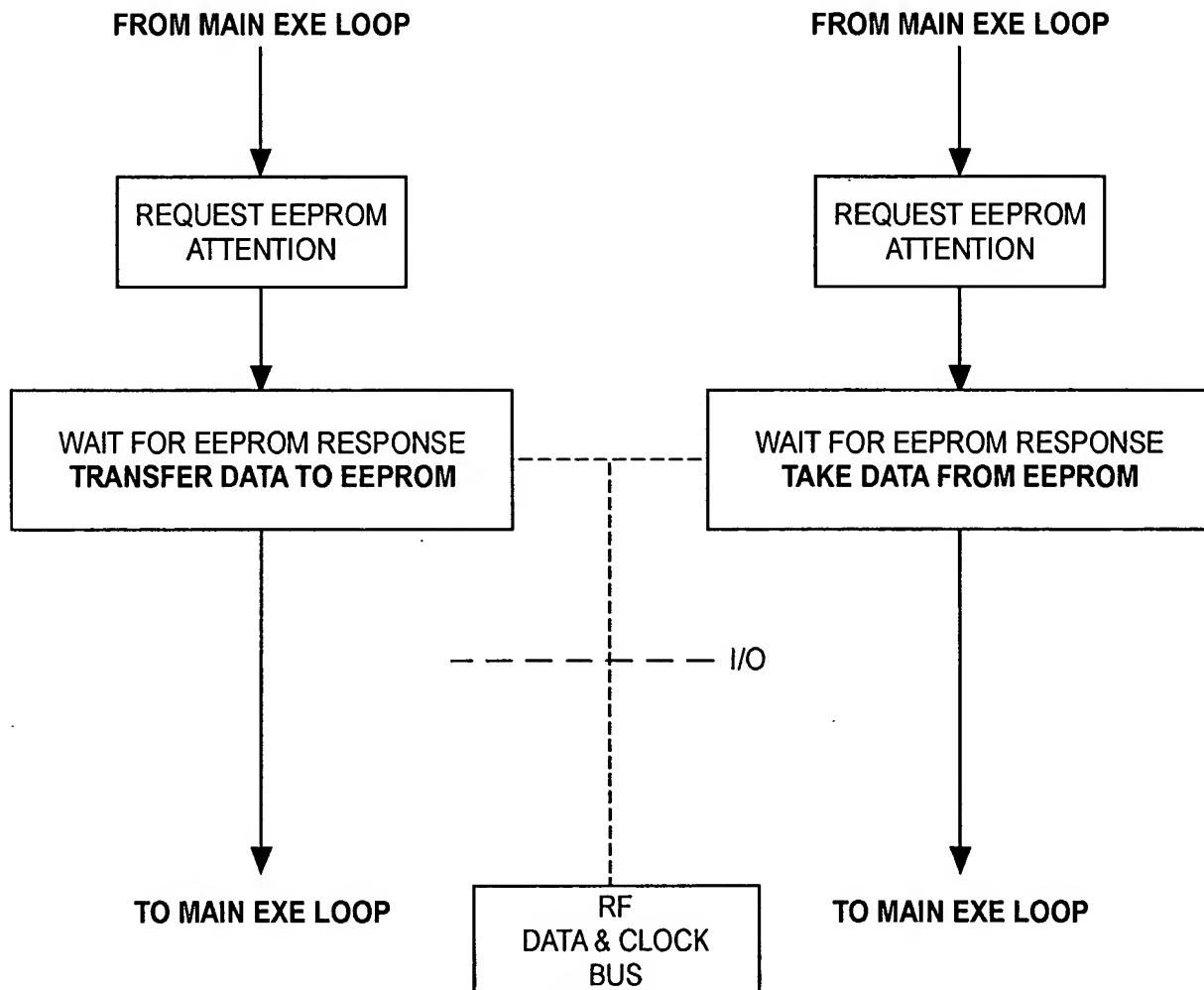


Fig. 25



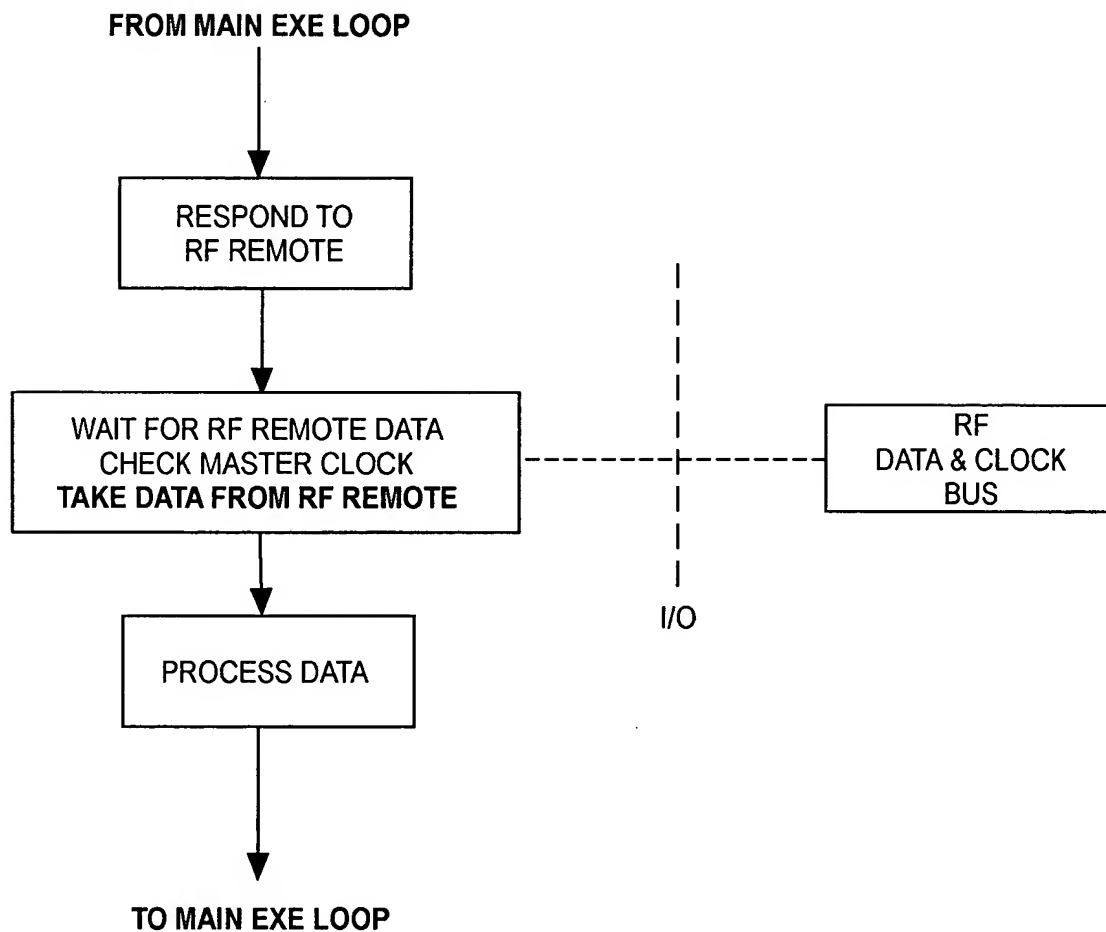
26/45

Fig. 26



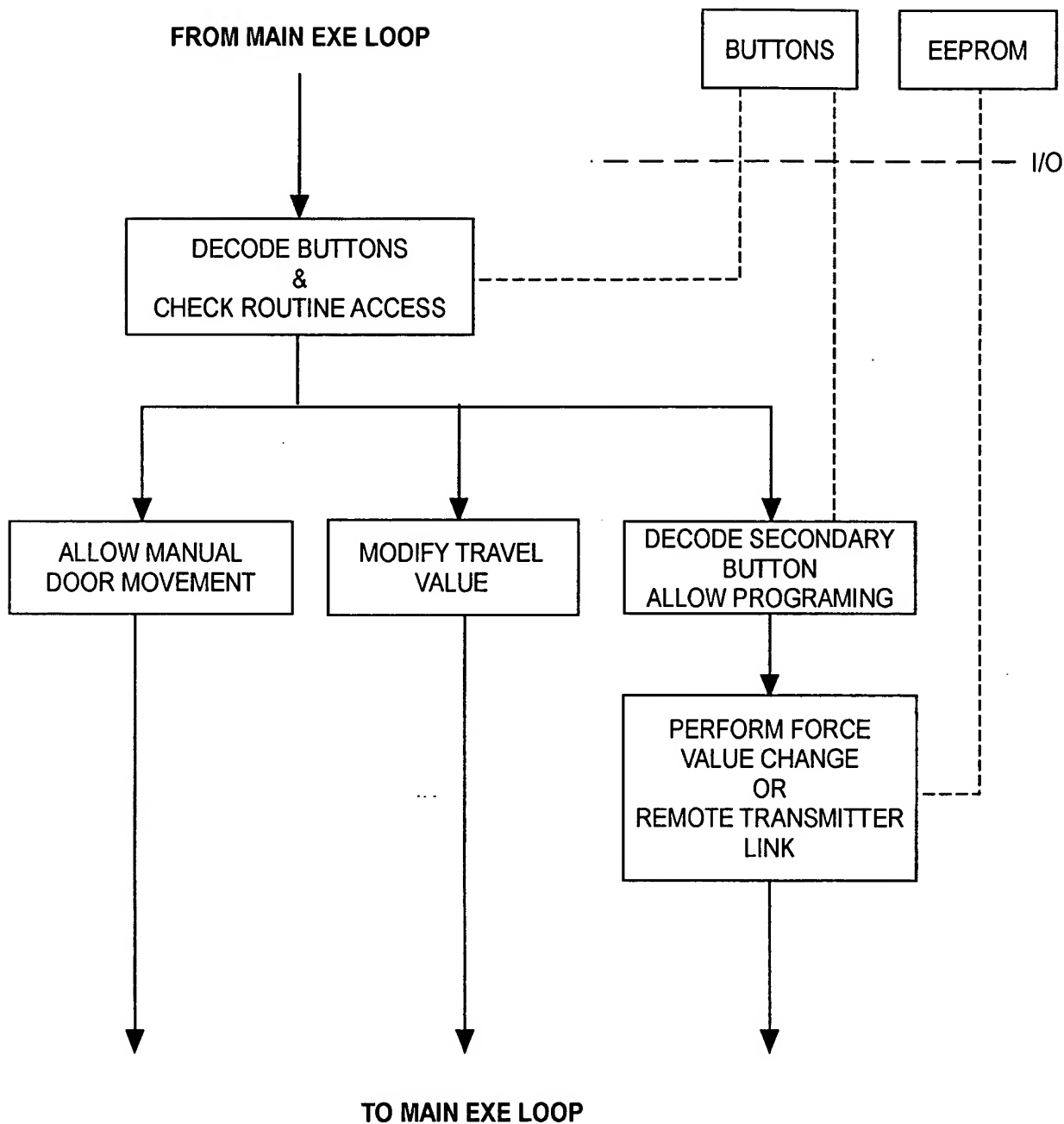
27/45

Fig. 27



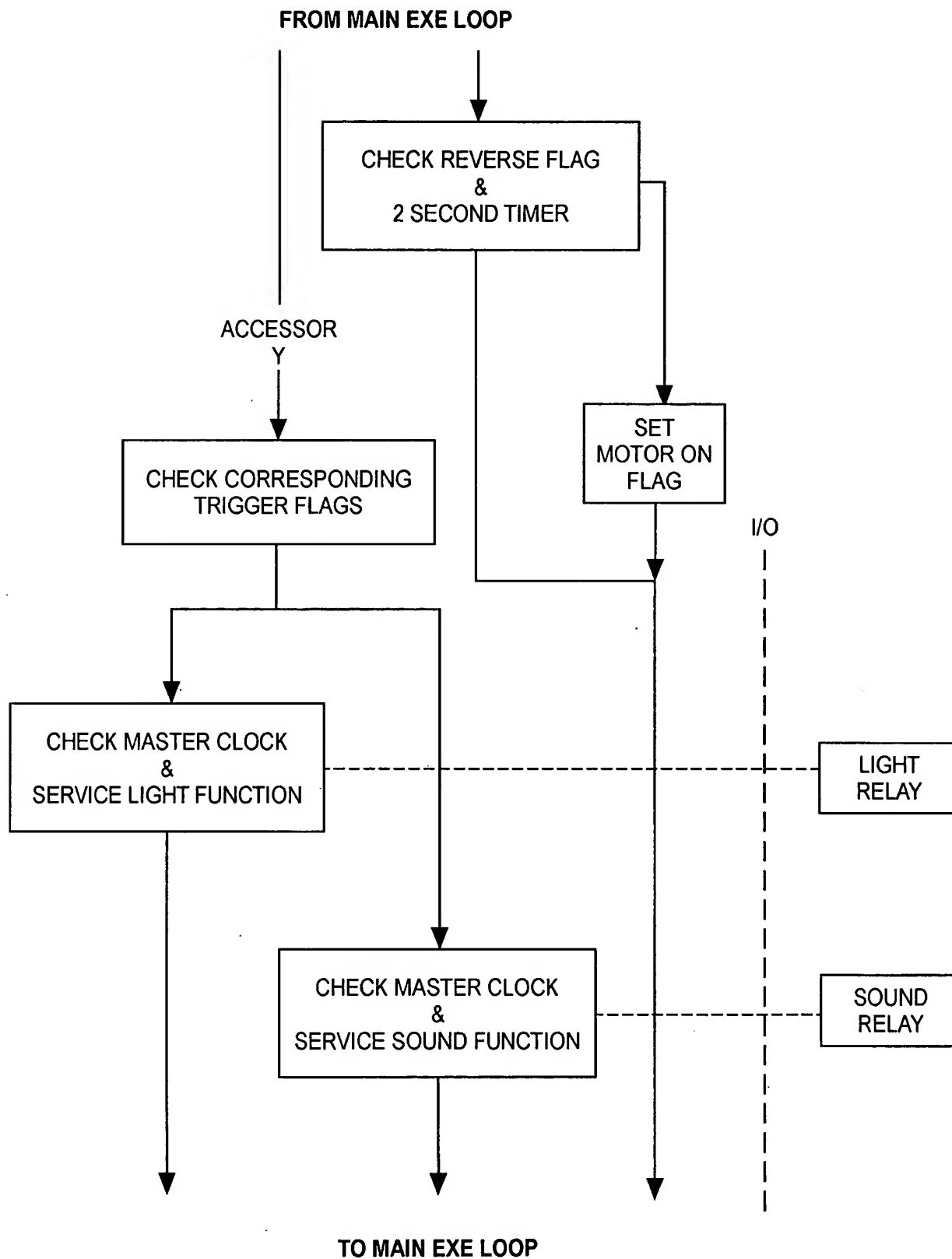
28/45

Fig. 28



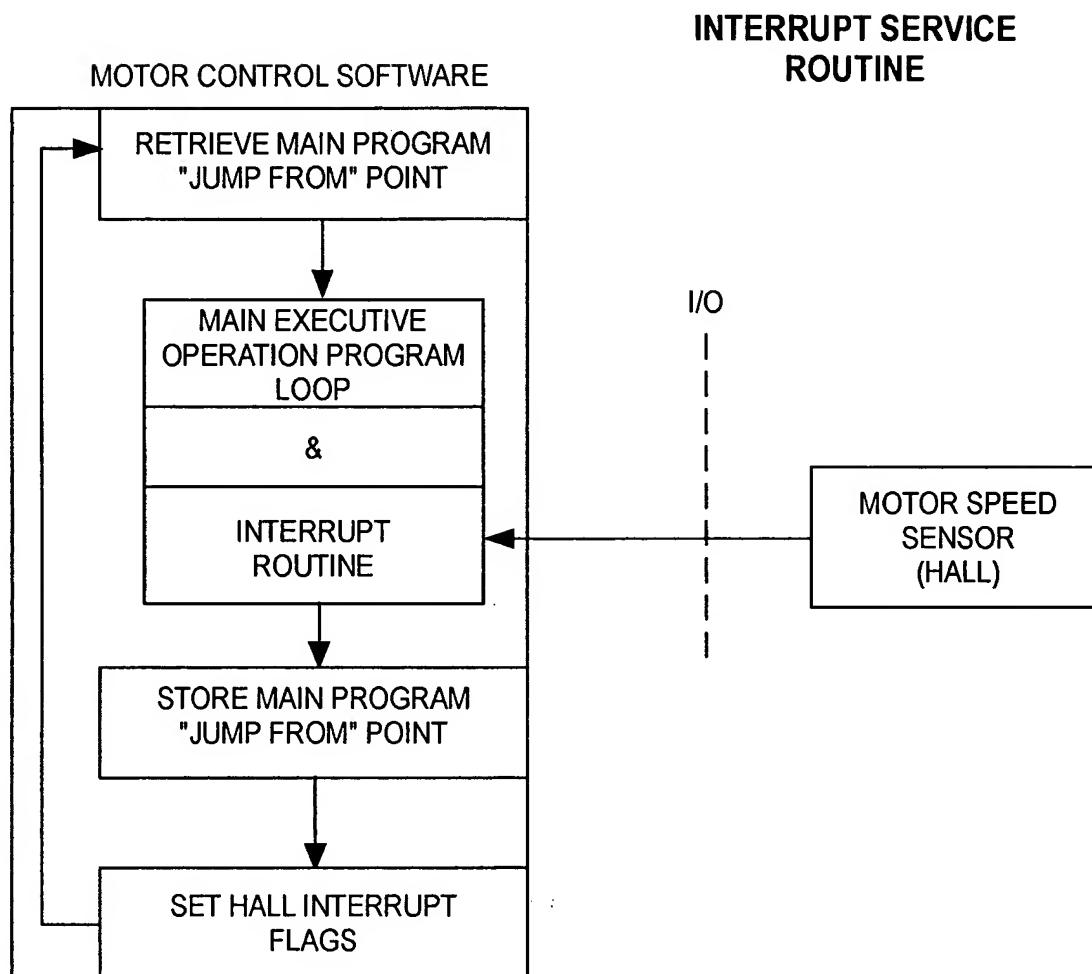
29/45

Fig. 29



30/45

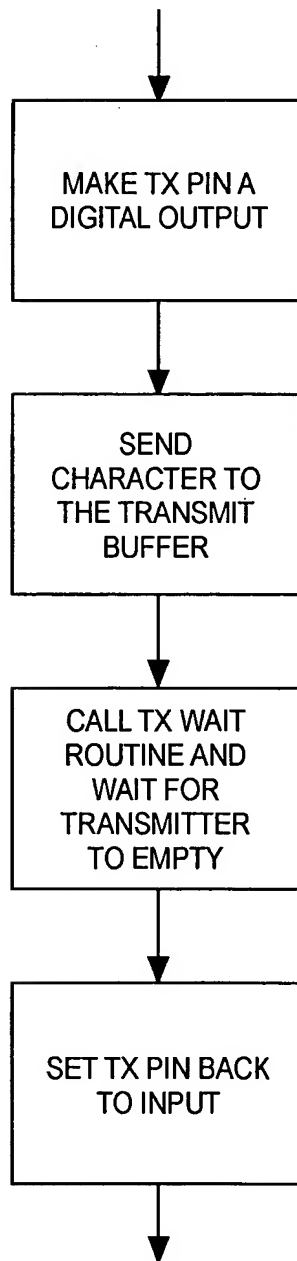
Fig. 30



31/45

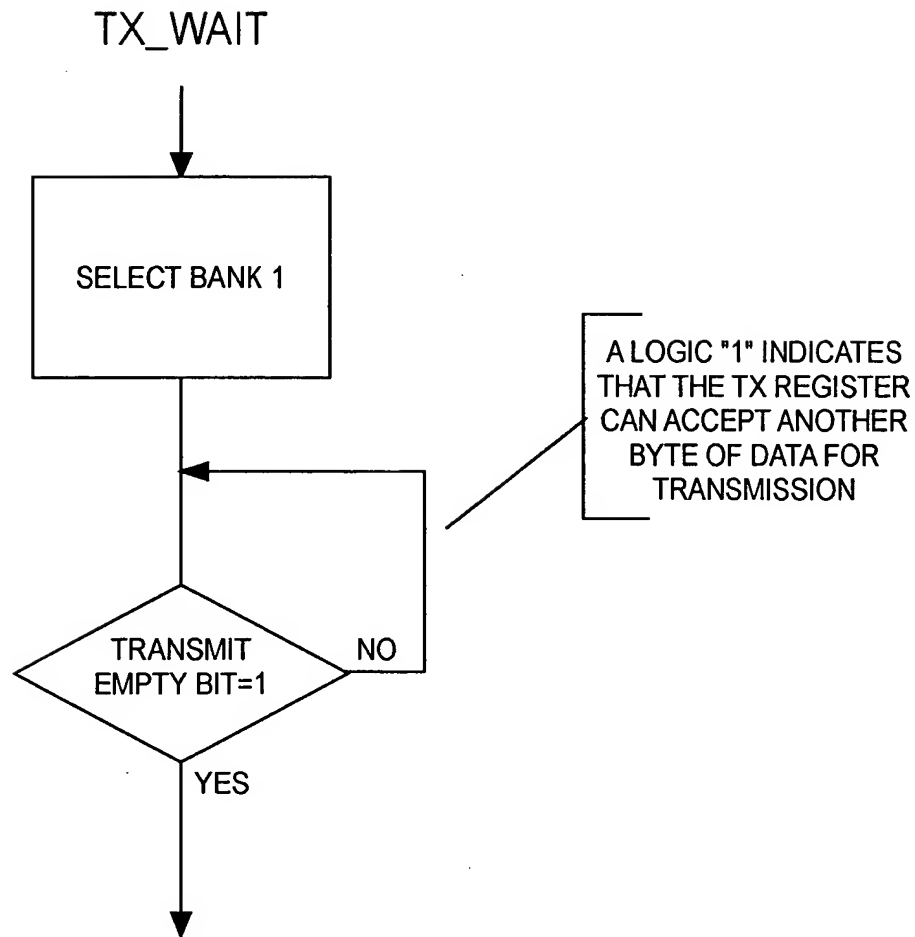
Fig. 31

SEND_USART



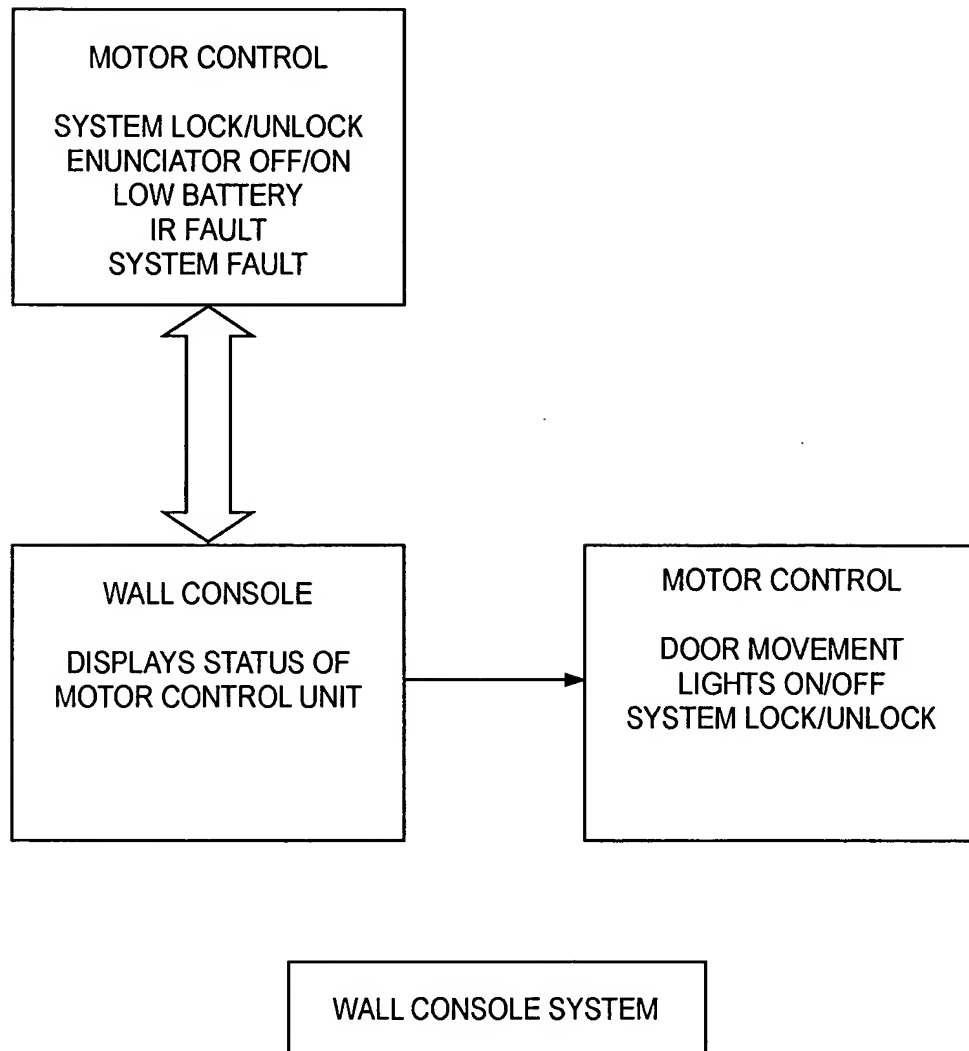
32/45

Fig. 32



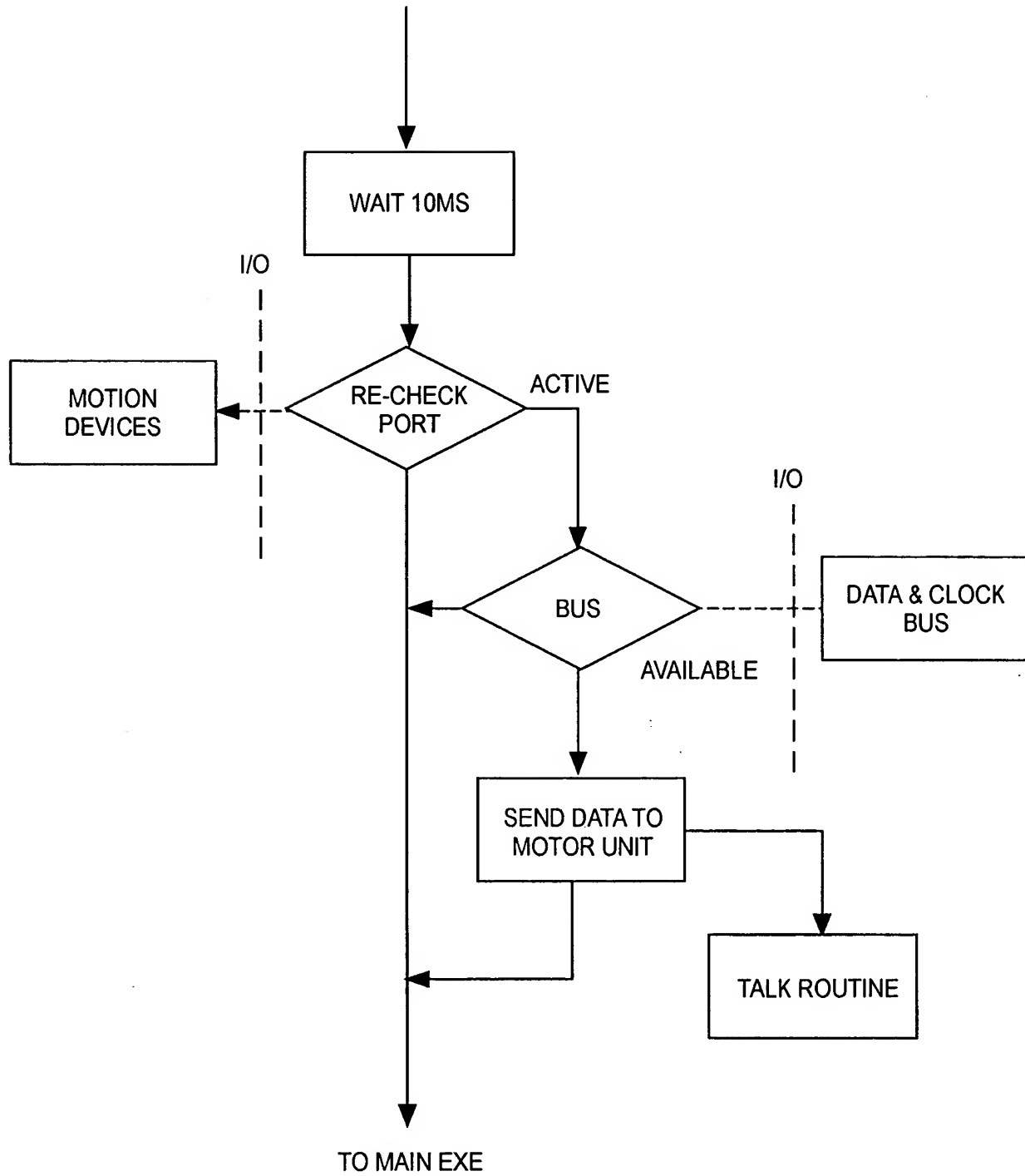
33/45

Fig. 33



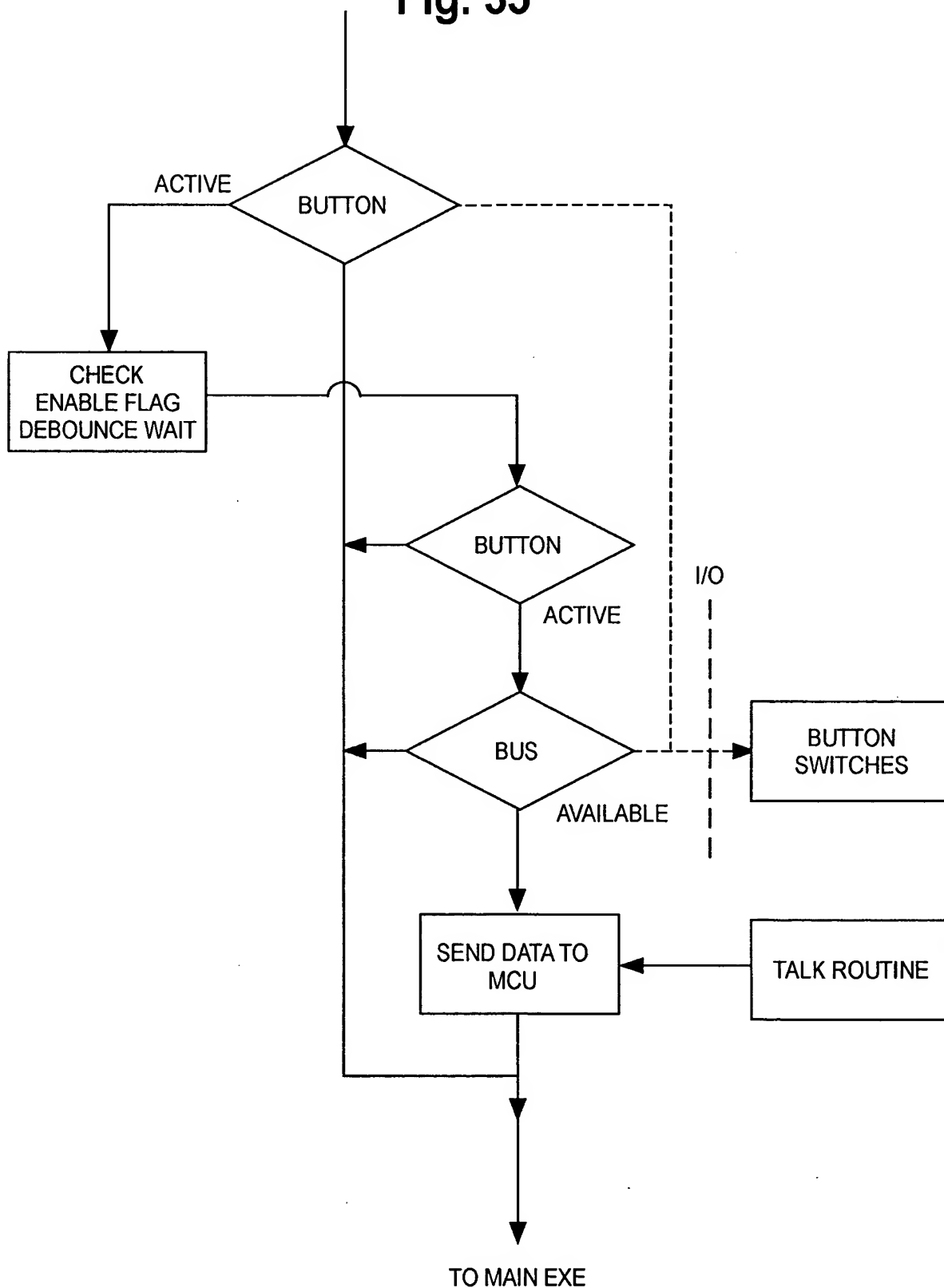
34/45

Fig. 34



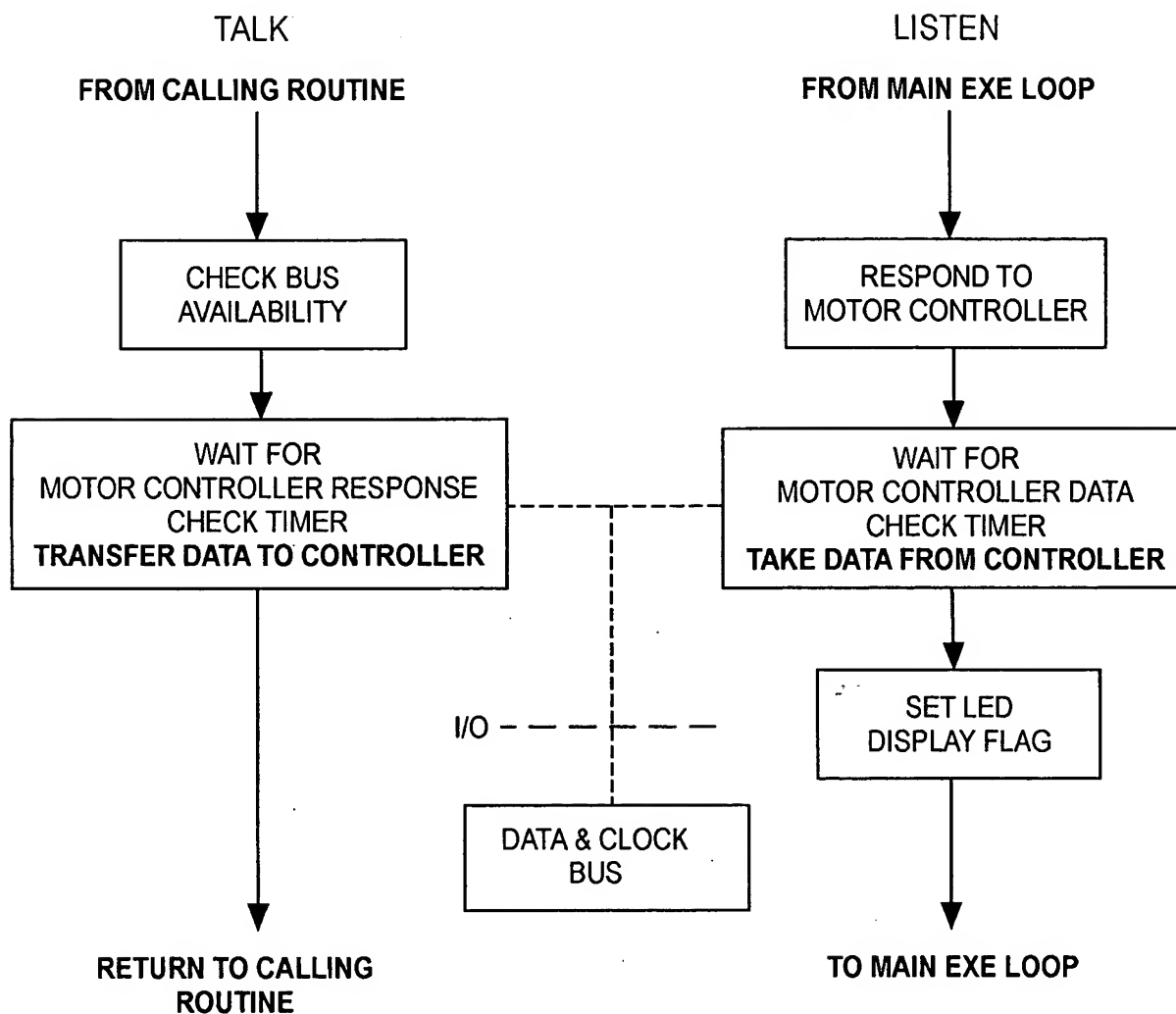
35/45

Fig. 35



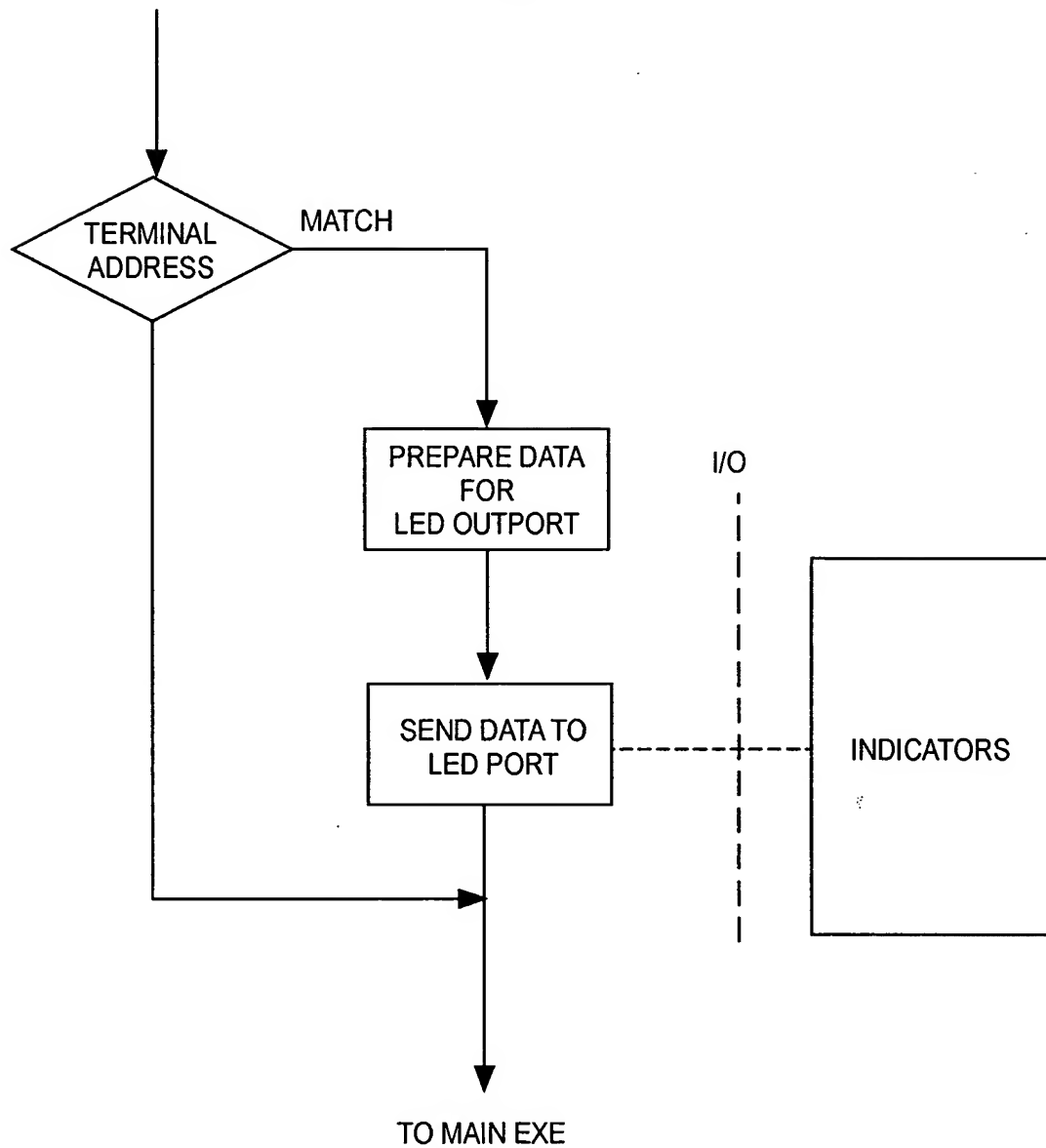
36/45

Fig. 36



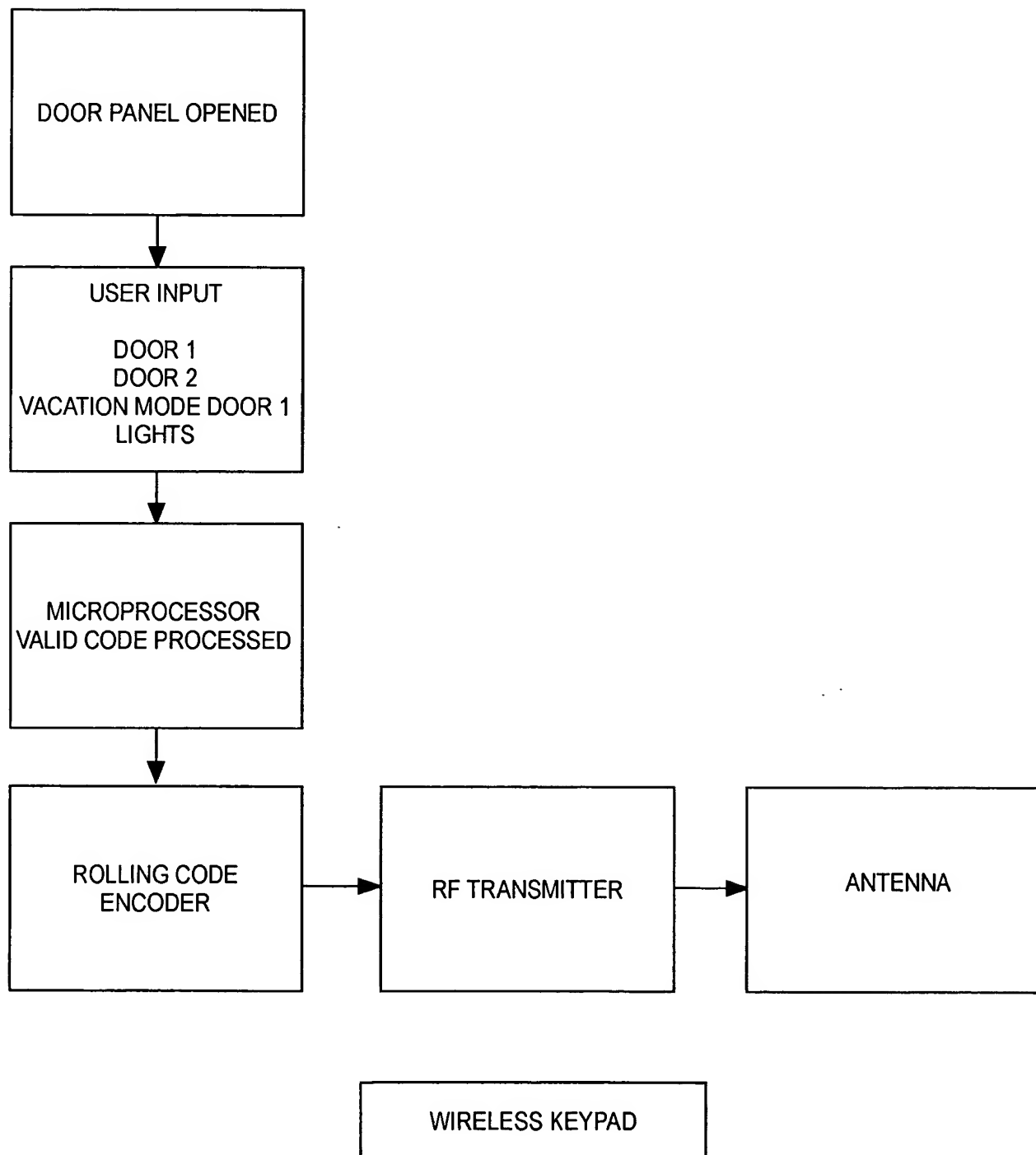
37/45

Fig. 37



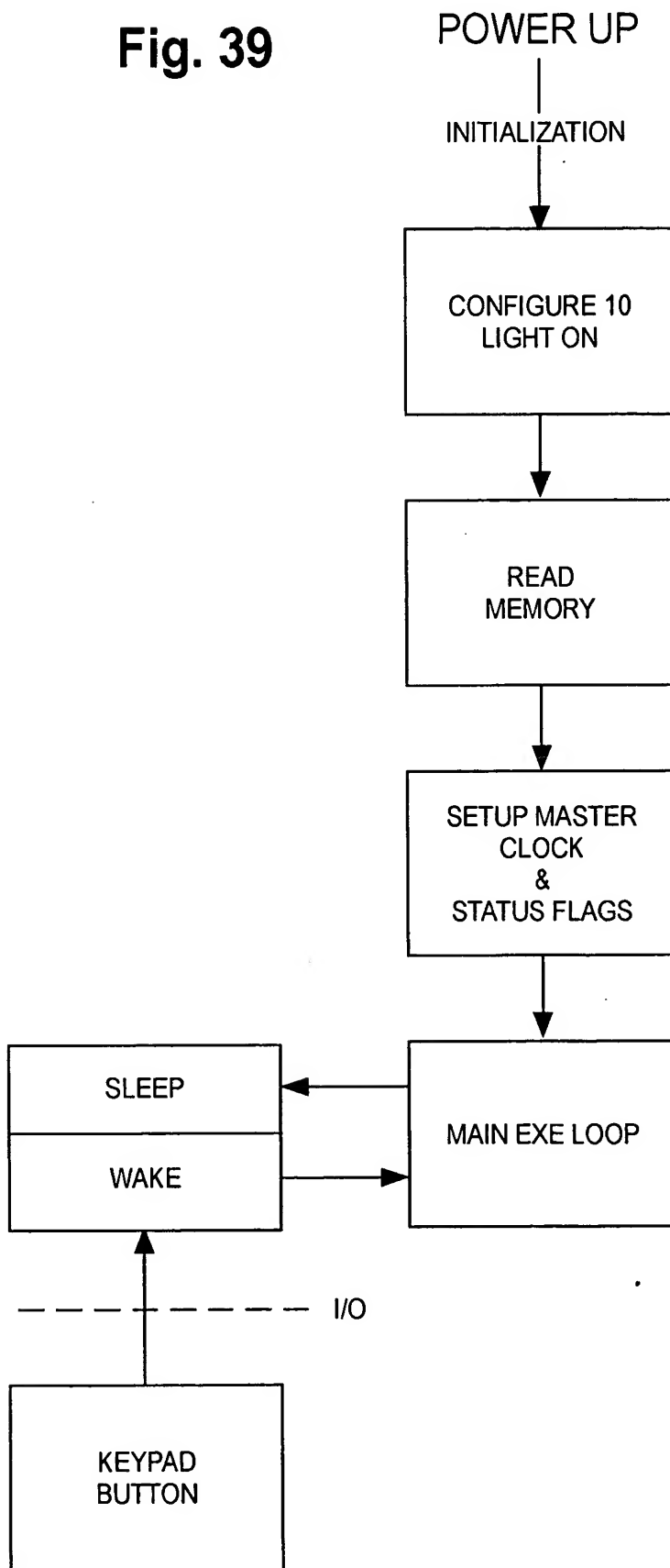
38/45

Fig. 38



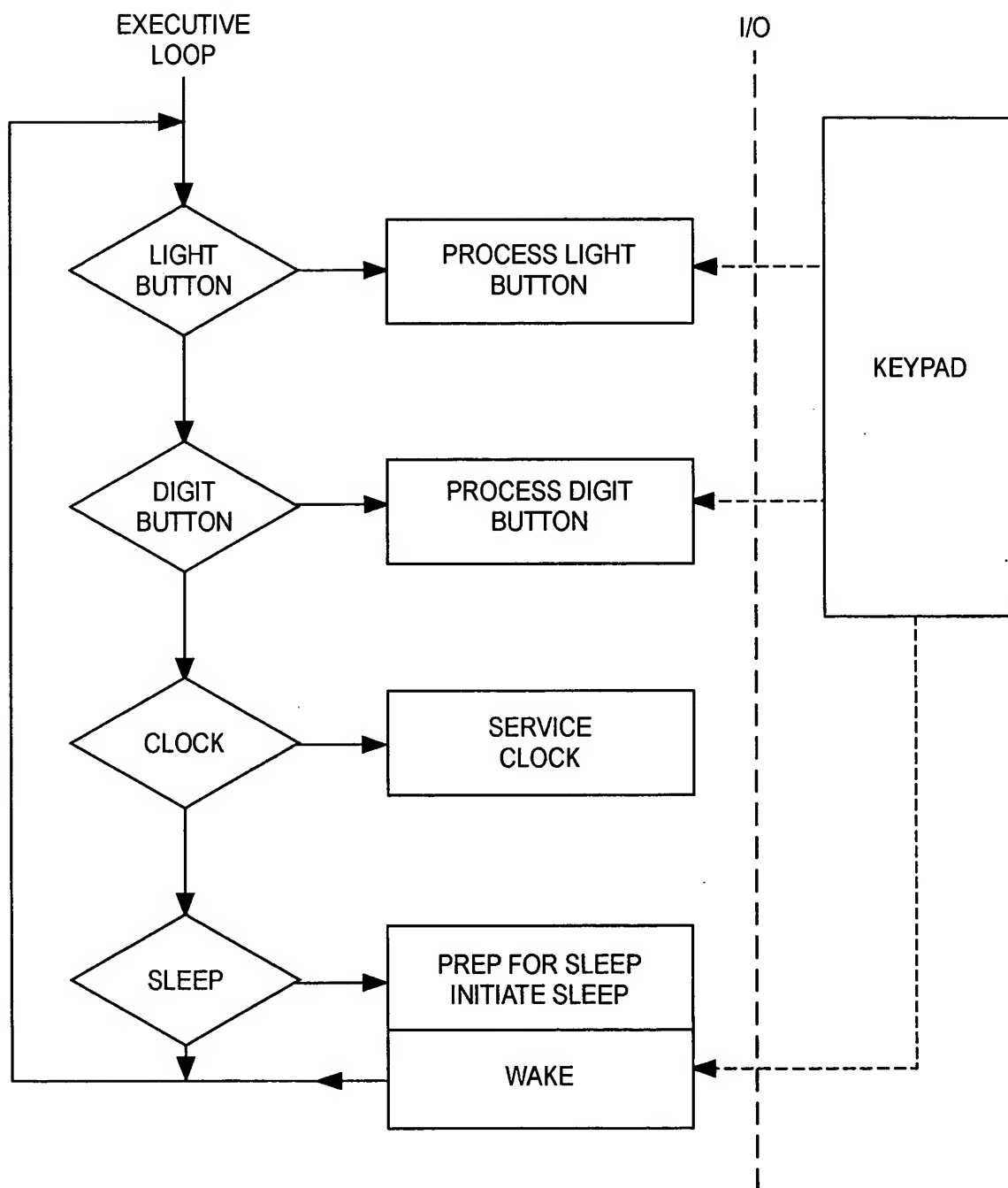
39/45

Fig. 39



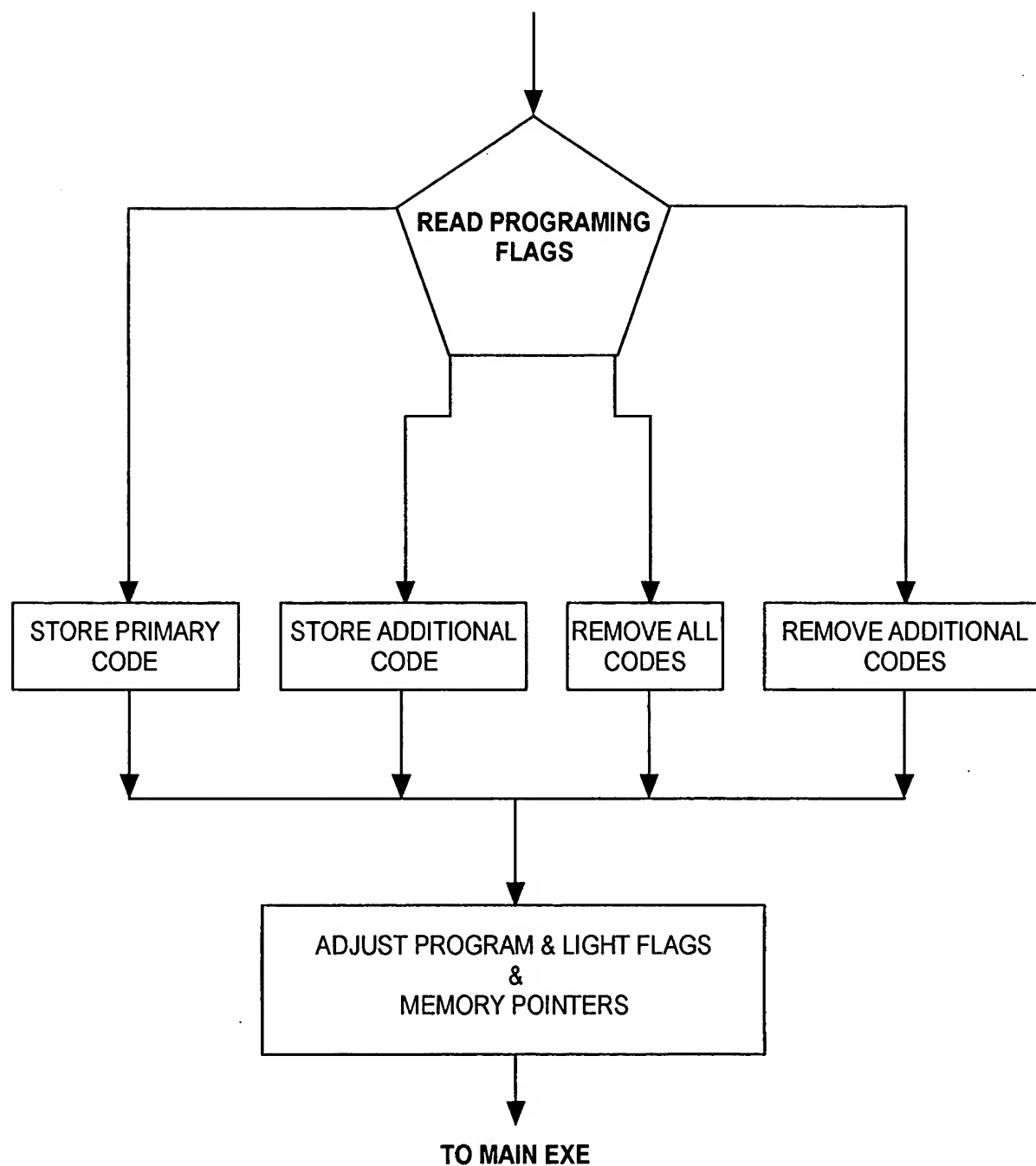
40/45

Fig. 40



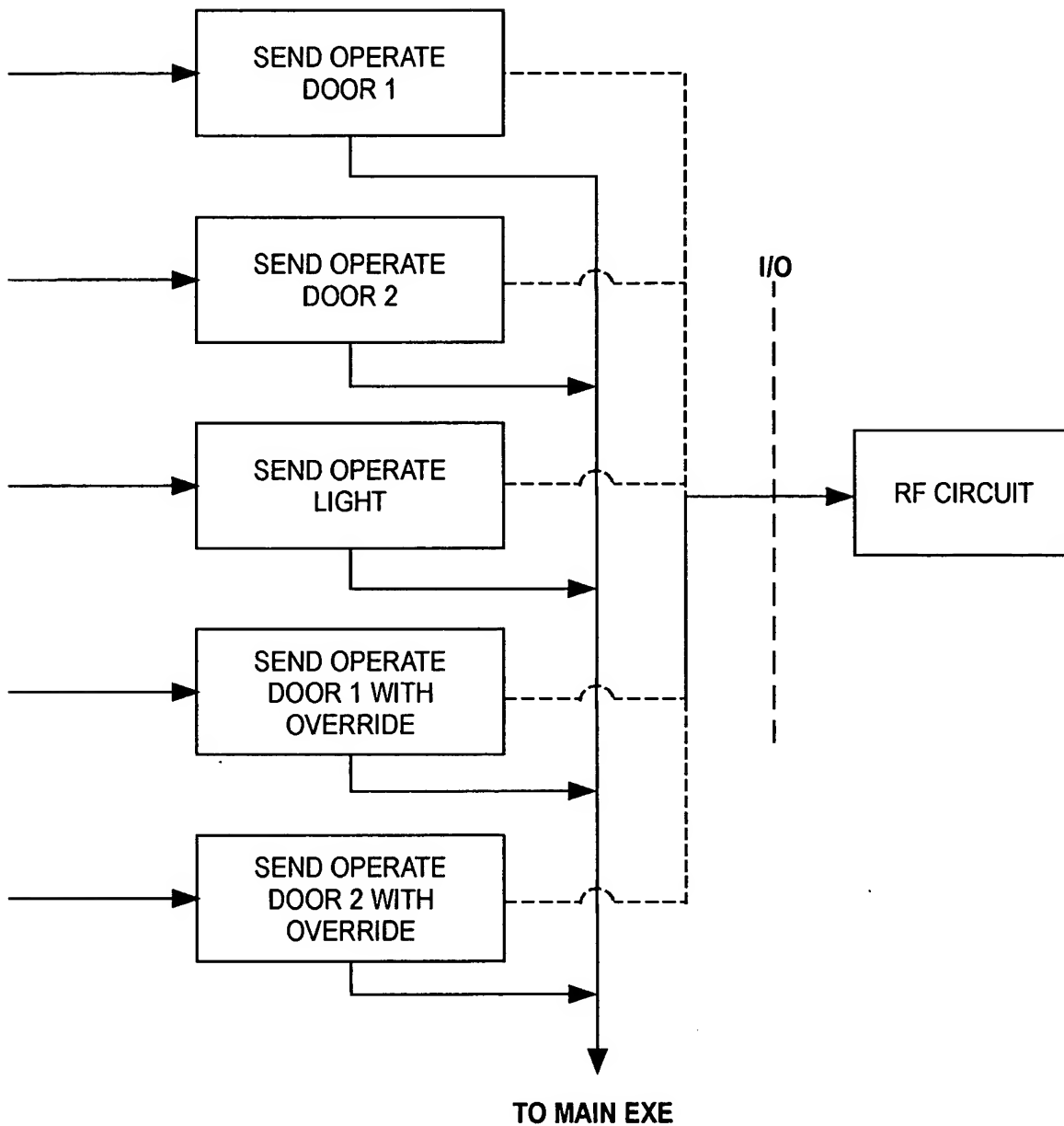
41/45

Fig. 41



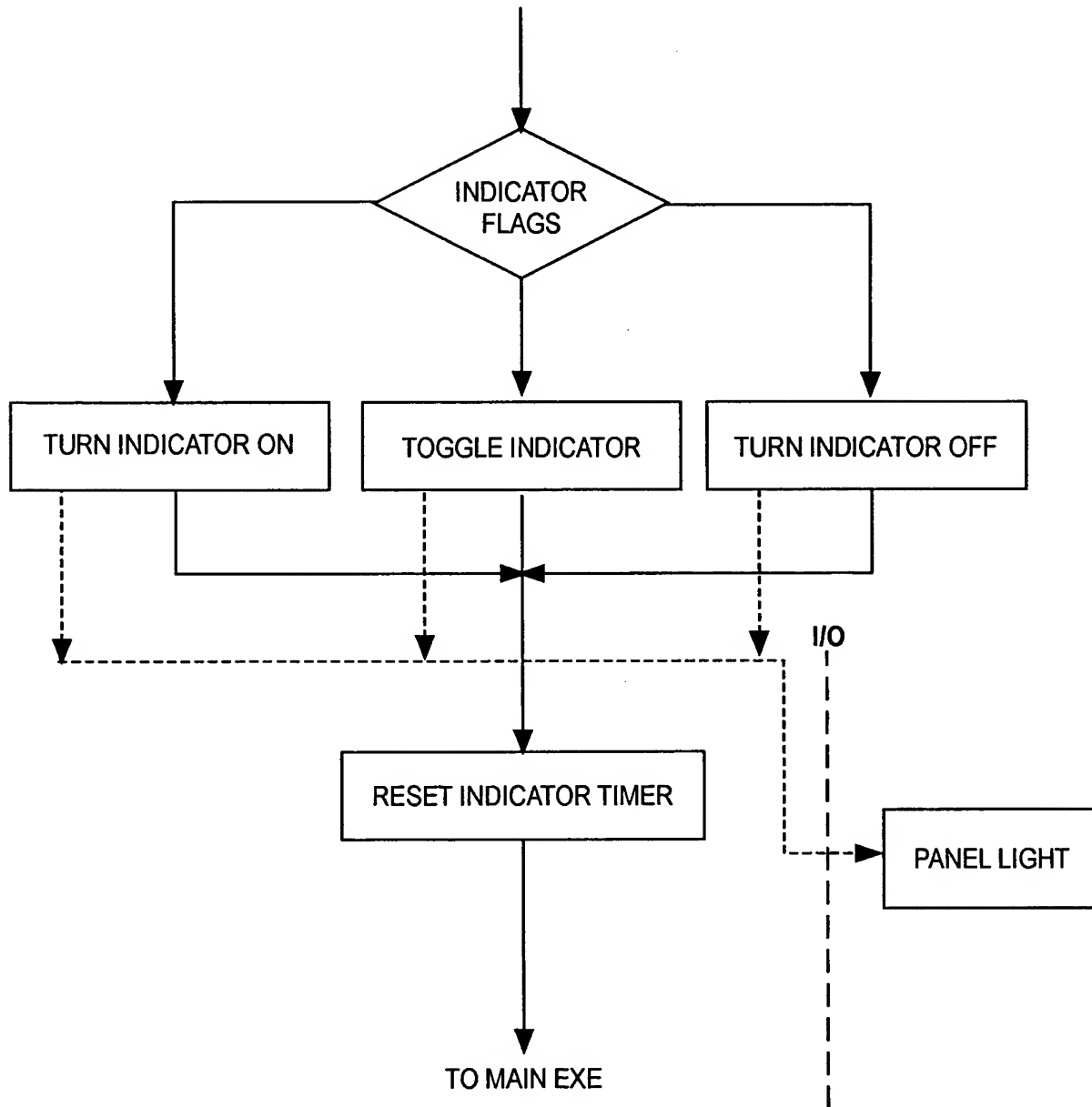
43/45

Fig. 43



44/45

Fig. 44



45/45

Fig. 45

